FACULTY OF ENGINEERING

Scheme of Instruction & Examination

(AICTE Model Curriculum for the Academic Year 2019-2020)

and

Syllabus

M.E. I to IV Semester

of

Two Year Post Graduate Degree Programme

in

Electronics and Communication Engineering Specialization in Embedded Systems

(With effect from the academic year 2019–2020) (As approved in the faculty meeting held on 25-06-2019)



Issued by

Dean, Faculty of Engineering Osmania University, Hyderabad – 500 007 2019

				Scheme of Instruction			So Exa	of tion	70	
S. No.	Course Type/Code	Course Name	L	Т	P/D	Contact Hrs/Wk	CIE	SEE	Duration in Hrs	Credits
Theory C	Courses									
1	Core	Program Core – I	3	1	-	4	30	70	3	4
2	Core	Program Core – II	3	-	-	3	30	70	3	3
3	Elective	Professional Elective – I	3	-	-	3	30	70	3	3
4	Elective	Professional Elective – II	3	-	-	3	30	70	3	3
5	MC or OE	Mandatory Course/Open Elective*	3	-	-	3	30	70	3	3
6	Audit	Audit Course – I	2	-	-	2	30	70	3	0
Practical	/ Laboratory	Courses						•		
7	Lab-I	Laboratory – I	-	-	2	2	25	50	3	1
8	PC 3254 ES	Seminar	-	-	2	2	25	50	3	1
		Total	17	01	04	22	230	520		18

SCHEME OF INSTRUCTION & EXAMINATION M.E. (Electronics and Communication Engineering) I – Semester Specialization in Embedded Systems

PC: Program CorePE: Professional ElectiveOE: Open ElectiveAD: Audit CourseMC: Mandatory CourseHS: Humanities and social scienceAD: Audit Course

L: Lecture	T: Tutorial	P : Practical	D : Drawing
CIE: Continuous Int	ernal Evaluation	SEE: Semester End E	xamination (Univ. Exam)

Note:

- 1. Each contact hour is a Clock Hour.
- 2. The practical class can be of two and half hour (clock hours) duration as per the requirement of a particular laboratory.
- 3. * If the Mandatory Course is offered in I-Semester, the Open Elective course should be offered in IIsemester. If Open Elective course is offered in I-Semester, then the Mandatory Course should be offered in II- semester.
- 4. ** Open Elective Subject is not offered to the students of ECE Department.

		Scheme of Instruction		So Exa	10					
S. No.	Course Type/Code	Course Name	L	Т	P/D	Contact Hrs/Wk	CIE	SEE	Duration in Hrs	Credits
Theory C	Courses									
1	Core	Program Core – III	3	1	-	4	30	70	3	4
2	Core	Program Core – IV	3	1	-	3	30	70	3	4
3	Elective	Professional Elective – III	3	-	-	3	30	70	3	3
4	MC or OE	Mandatory Course/Open Elective*	3	-	-	3	30	70	3	3
5	Audit	Audit Course – II	2	-	-	2	30	70	3	0
Practical	/ Laboratory	Courses								
6	Lab-II	Laboratory – II	-	-	2	2	25	50	3	1
7	Lab-III	Laboratory – III	-	-	2	2	25	50	3	1
8	PC 3255 ES	Mini Project with Seminar	-	-	4	4	25	50	3	2
		Total	14	02	08	24	300	450		18

SCHEME OF INSTRUCTION & EXAMINATION M.E. (Electronics and Communication Engineering) II – Semester Specialization in Embedded Systems

PC: Program Core	PE: Professional Elective	OE : Open Elective	AD: Audit Course
MC: Mandatory Course	HS: Humanities and social	science	

L: Lecture	T: Tutorial	P : Practical	D : Drawing
CIE: Continuous In	ternal Evaluation	SEE: Semester End H	Examination (Univ. Exam)

Note:

- 1. Each contact hour is a Clock Hour.
- 2. The practical class can be of two and half hour (clock hours) duration as per the requirement of a particular laboratory.
- 3. ** Open Elective Subject is not offered to the students of ECE Department.

SCHEME OF INSTRUCTION & EXAMINATION M.E. (Electronics and Communication Engineering) III – Semester Specialization in Embedded Systems

		Course Name		Sch Inst	eme o ructio	f n	So Exa	cheme aminat	of tion	
S. No.	Course Type/Code			Т	P/D	Contact Hrs/Wk	CIE	SEE	Duration in Hrs	Credits
Theory C	Courses									
1	Elective	Professional Elective – IV	3	-	-	3	30	70	3	3
2	Elective	Professional Elective – V	3	-	-	3	30	70	3	3
3	PC 3256 ES	Major Project Phase – I	-	-	20	20	100	-	3	10
		Tota	ul 06	-	20	26	160	140		16

M.E. (Electronics and Communication Engineering) IV – Semester Specialization in Embedded Systems

		Course Name		Scheme of Instruction		f n	Scheme of Examination			
S. No.	Course Type/Code			Т	P/D	Contact Hrs/Wk	CIE	SEE	Duration in Hrs	Credits
Theory C	Courses									
1	PC 3257 ES	Major Project Phase – II (Dissertation)	-	-	32	32	-	200	3	16
		Total	-	-	32	32	-	200		16

PC: Program Core	PE: Professional Elective	OE : Open Elective	AD: Audit Course
MC: Mandatory Course	HS: Humanities and social	science	

L: Lecture	T : Tutorial	P : Practical	D : Drawing
CIE: Continuous Inte	rnal Evaluation	SEE: Semester End E	Examination (Univ. Exam)

Note:

- 1. Each contact hour is a Clock Hour
- 2. The practical class can be of two and half hour (clock hours) duration as per the requirement of a particular laboratory.
- 3. ** Open Elective Subject is not offered to the students of ECE Department.
- 4. The students who are willing to register for MOOCs in the M.E. (SE) III semester instead of Professional Electives – IV & V, should register for those of the courses, approved by the CBoS, OU and respective college MOOCs Coordinator. Those students are strictly not permitted to appear for either CIE or SEE of Professional Electives – IV & V if they abstain from attending the semester class work. Further, for students willing to appear for both MOOCS and Professional Electives, they should fulfill the

minimum attendance criteria.

S. No.	Course Code	Course Title
1	PC 3301 EV	Micro Controllers for Embedded System Design
2	PC 3202 ES	Smart Sensors and Internet of Things
3	PC 3203 ES	Programming and Interfacing with Microcontroller
4	PC 3204 ES	IoT Applications and Communication Protocols

List of subjects of Professional Core

List of subjects of Professional Electives I to V

S. No.	Course Code	Course Title
1	PE 3216 ES	Wireless Sensor Protocols and Programming
2	PE 3217 ES	Advance Wireless and Mobile Networks
3	PE 3218 ES	Wireless Access Technologies
4	PE 3219 ES	Embedded Linux and Basics of Device Drivers
5	PE 3220 ES	Neural Networks and Fuzzy Logic
6	PE 3221 ES	Privacy and Security in IoT
7	PE 3222 ES	IoT: Sensing and Actuator Devices
8	DE 2002 ES	Energy Harvesting Technology and Power Management for IoT
	FE 3223 ES	Devices
9	PE 3224 ES	Scripting Languages
10	PE 3225 ES	Image and Video Processing
11	PE 3226 ES	Kernel and Driver Programming
12	PE 3227 ES	Cloud Computing
13	PE 3228 ES	Mobile Computing
14	PE 3319 EV	SoC Design
15	PC 3303 EV	Real Time Operating Systems (Elective)

List of Mandatory Courses

S. No.	Course Code	Course Title
1	MC 5161 ME	Research Methodology & IPR

List of Open Electives

S. No.	Course Code	Course Title
1	OE 9101 CE	Cost Management of Engineering Projects
2	OE 9102 CS	Business Analytics
3	OE 9103 EC**	Embedded System Design
4	OE 9104 EE	Waste to Energy
5	OE 9105 ME	Industrial Safety

Note: ** Open Elective Subject is not offered to the students of ECE Department.

List of subjects of Audit Course-I

S. No.	Course Code	Course Title
1	AD 9001 HS	English for Research Paper Writing
2	AD 9002 CE	Disaster Management
3	AD 9003 HS	Sanskrit for Technical Knowledge
4	AD 9004 HS	Value Education

S. No.	Course Code	Course Title
1	AD 9011 HS	Constitution of India and Fundamental Rights
2	AD 9012 HS	Pedagogy Studies
3	AD 9013 HS	Stress Management by Yoga
4	AD 9014 HS	Personality Development through life Enlightenment Skills

List of subjects of Audit Course-II

List of Laboratory Courses

S. No.	Lab No.	Course Code	Course Title
1	Ι	PC 3151 ES	Embedded Systems Lab – I
2	II	PC 3152 ES	IoT Lab
3	III	PC 3153 ES	Embedded Systems Lab – II

Course Code			Core/Elective				
PC 3301 EV	Mi	crocontr	Core				
Prerequisite	C	ontact Hou	ırs per We	æk	CIE	SEE	Credits
rierequisite	L	Т	D	Р		SEE	Credits
-	3	1	70	4			

- > Detailed overview of important concepts of Embedded system
- > Analyse PIC microcontroller, its features and programming
- > Describe ARM Microcontroller architectural details and instruction set
- Understand ARM Memory management
- > Learn the techniques to develop an embedded system and case studies

Course Outcomes

After completing this course, the student will be able to:

- 1. Define an embedded system with an overview of important concepts and trends in the design process along with the challenges faced in the embedded systems design.
- 2. Understand the architecture of PIC 18 Microcontroller, its features and programming.
- 3. Understand ARM Design Philosophy, architectural details, instruction set and ARM Memory Management.
- 4. Analyse and compare the utility and effectiveness of various debugging tools and techniques.
- 5. Design a real time based embedded system in the area of communication, automotive, etc.

UNIT I

Introduction to Embedded Systems: Overview of Embedded System Architecture, Challenges & Trends of Embedded Systems, Hardware Architecture, Software Architecture. Application areas of Embedded Systems and Categories of Embedded Systems. Embedded System Design and Co-Design issues and Design Cycle Process

UNIT II

PIC 18: Family Overview, Architecture, Instruction Set, Addressing modes. Timers, interrupts of PIC 18, Capture/Compare and PWM modules of PIC 18

UNIT III

ARM Architecture: ARM Design Philosophy, Registers, Program Status Register, Instruction Pipeline, Interrupts and Vector Table, Architecture Revision, ARM Processor Families. Instruction Set: Data Processing Instructions, Addressing Modes, Branch, Load, Store Instructions, PSR Instructions, Conditional Instructions.

UNIT IV

ARM Thumb Instruction Set: Register Usage, Other Branch Instructions, Data Processing Instruction Single-Register and Multi Register Load-Store Instructions, Stack, Software Interrupt Instructions. Exception and interrupt handling.

ARM Memory Management: Cache Architecture, Polices, Flushing and Caches, MMU, Page Tables, Translation Access Permissions, Context Switch.

UNIT V

Embedded Software Development Tools, Host and Target Machines, Linkers/Locators for Embedded Software, Getting Embedded Software into the Target System. Debugging Techniques.

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Case Studies: Design of Embedded Systems using Microcontrollers – for applications in the area of communication and automotive. (GSM/GPRS, CAN, ZigBee)

- 1. Raj Kamal, Embedded Systems Architecture, Programming and Design, 2nd Edition, TMH, 2008.
- 2. Andrew N. Sloss, Dominic Symes, Chris Wright, ARM Systems Developer's Guides Designing & Optimizing System Software, Elsevier, 2008.
- 3. Mazidi, MCKinlay and Danny Causey, *PIC Microcontrollers and Embedded Systems*, Pearson Education, 2007
- 4. David.E. Simon, An Embedded Software Primer, 1st Edition, Pearson Education, 1999
- 5. Jonathan W. Valvano, *Embedded Microcomputer Systems, Real Time Interfacing*, Thomas Learning, 1999.

Course Code			Core/Elective				
PC 3202 ES		Smart	Core				
Prerequisite	C	ontact Hou	ırs per We	æk	CIE	SEE	Credits
rierequisite	L	Т	D	Р	CIE	SEE	Credits
-	3	4					

- > Able to understand the application areas of IoT
- > Able to realize the revolution of Internet in Mobile Devices, Cloud & Sensor Networks
- > Able to understand building blocks of Internet of Things and characteristics
- > Able to understand smart sensors principles
- Able to interface smart sensors

Course Outcomes

After completing this course, the student will be able to:

- 1. Understand the vision of IoT from a global context
- 2. Use of Devices, Gateways and Data Management in IoT.
- 3. Application of IoT in Industrial and Commercial Building Automation and Real World Design Constraints.
- 4. Understand different sensor architectures
- 5. Develop different types of applications by using sensors

UNIT I

Environmental Parameters Measurement and Monitoring: Why measurement and monitoring are important, effects of adverse parameters for the living being for IoT

UNIT II

Sensors: Working Principles: Different types; Selection of Sensors for Practical Applications, Introduction of Different Types of Sensors such as Capacitive, Resistive, Surface Acoustic Wave for Temperature, Pressure, Humidity, Toxic Gas etc.

UNIT III

Important Characteristics of Sensors: Determination of the Characteristics Fractional order element: Constant Phase Impedance for sensing applications such as humidity, water quality, milk quality Impedance Spectroscopy: Equivalent circuit of Sensors and Modelling of Sensors Importance and Adoption of Smart Sensors

UNIT IV

Architecture of Smart Sensors: Important components, their features Fabrication of Sensor and Smart Sensor: Electrode fabrication: Screen printing, Photolithography, Electroplating Sensing film deposition: Physical and chemical Vapour, Anodization, Sol-gel

UNIT V

Interface Electronic Circuit for Smart Sensors and Challenges for Interfacing the Smart Sensor, Usefulness of Silicon Technology in Smart Sensor and Future scope of research in smart sensor

- 1. Yasuura, H., Kyung, C.-M., Liu, Y., Lin, Y.-L., Smart Sensors at the IoT Frontier, Springer International Publishing
- 2. Kyung, C.-M., Yasuura, H., Liu, Y., Lin, Y.-L., Smart Sensors and Systems, Springer International Publishing

Course Code			Core/Elective						
PC 3203 ES	Prog	ramming	Core						
Prerequisite	C	ontact Hou	ırs per We	ek	CIE	SEE	Credits		
Trerequisite	L	Т	D	Р	CIL	SEE	Credits		
-	3	3 1 30 70							

- > Understand advanced and emerging networking technologies
- > Obtain skills to do advanced networking research and programming
- > Learn how to use software programs to perform varying and complex networking tasks
- > Expand upon the knowledge learned and apply it to solve real world problems
- Learn IoT protocols

Course Outcomes

After completing this course, the student will be able to:

- 1. Summarise different open source platforms and open frameworks
- 2. Study different communication protocols
- 3. Design a specific application by using sensors and actuators
- 4. learn different messaging standards for communication over the internet
- 5. Learn basic communication techniques to send data to cloud

UNIT I

Introduction – History - Creative Coding Platforms - Open Source Platforms – PIC - Arduino, Sketch, Raspberry Pi, Iterative coding methodology – Python Programming - Mobile phones and similar devices -Arm Devices - Getting used to Arduino - Sensor Characterization: Safety, Basic Electronics (circuit theory, measurements, parts identification) Sensors and Software: Understanding Processing Code Structure, variables and flow control, Interfacing to the Real World

UNIT II

Software: open Frameworks as our IDE (C/C++) - "Arduino" Language (C/C++) - Hardware: Desktop / Laptop / Raspberry Pi - How to approach a programming problem? Representing "reality" with computers. Digital vs. Analog circuits, audio, communication, etc. Analog to Digital Conversion (ADC) - Digital to Analog Conversion (DAC) – Microcontrollers

UNIT III

Communication – Serial& Parallel - Hardware to Hardware Communication - I2C/IIC (Inter-Integrated Circuit) - SPI (Serial Peripheral Interface) – Serial UART Communication - Introduction to the command line – git/GitHub. Introduction to Programming: A comparative studio between Arduino + open Frameworks - Arduino-compatible Microcontrollers Sensors and Actuators

UNIT IV

Advanced I/O – open Frameworks: Live Network feeds (push and pull) - Data persistence (saving data and preferences) - Database interface (MySQL, SQLite, XML, PHP/Web) - Arduino: Wired/Wireless Networking

UNIT V

Talking to the cloud: Baby steps to Internet of Things, TCP/IP and UDP - Building peer to peer communication system using Bluetooth & WiFi - Experiments

- 1. Programming Interactivity, Second Edition by Josha Noble, 2012
- 2. Programming the Raspberry Pi: Getting Started with Python 2E, 2016

Course Code			Core/Elective				
PC 3204 ES	ІоТ	Applica	Core				
Prerequisite	C	ontact Hou	ırs per We	ek	CIE	SEE	Cradita
Terequisite	L	Т	D	Р	CIL	SEE	Credits
-	3	-	-	-	30	70	3

- Basic introduction of all the elements of IoT-Mechanical, Electronics/sensor platform, Wireless and wireline protocols
- > Mobile to Electronics integration, Mobile to enterprise integration
- > Open source/commercial electronics platform for IoT-Raspberry Pi, Arduino, ArmMbedLPC
- Open source /commercial enterprise cloud platform for IoT-Ayla, iO Bridge, Libellium, Axeda, Cisco fog cloud
- Learn different M2M platforms

Course Outcomes

After completing this course, the student will be able to:

- 1. To understand merging technological options, platforms and case studies of IoT implementation in home & city automation
- 2. Understand database implementation for IoT
- 3. Determine the Market perspective of IoT
- 4. Experience with working of cloud computing service
- 5. Design a specific application by using IoT

UNIT I

Basic function and architecture of a sensor — sensor body, sensor mechanism, sensor calibration, sensor maintenance, cost and pricing structure, legacy and modern sensor network.

Development of sensor electronics — IoT vs legacy, and open source vs traditional PCB design style Development of sensor communication protocols, Protocols: Modbus, relay, ZigBee, Zwave, X10, Bluetooth, ANT, etc. Business driver for sensor deployment — FDA/EPA regulation, fraud/tempering detection, supervision, quality control and process management

Different kind of calibration Techniques: manual, automation, infield, primary and secondary calibration — and their implication in IoT Powering options for sensors: battery, solar, Witricity, Mobile and PoE

UNIT II

ZigBee and Zwave — advantage of low power mesh networking. Long distance ZigBee. Introduction to different ZigBee chips. Bluetooth/BLE: Low power vs high power, speed of detection, class of BLE. Introduction of Bluetooth vendors & their review. Wireless protocols such as Piconet and packet structure for BLE and ZigBee Other long distance RF communication link. LOS vs NLOS links, Capacity and throughput calculation Application issues in wireless protocols: power consumption, reliability, PER, QoS, LOS

UNIT III

PCB vs FPGA vs ASIC design Prototyping electronics vs Production electronics QA certificate for IoT-CE/CSA/UL/IEC/RoHS/IP65 Basic introduction of multi-layer PCB design and its workflow Electronics reliability-basic concept of FIT and early mortality rate Environmental and reliability testing-basic concepts Basic Open source platforms: Arduino, Raspberry Pi, Beaglebone

Faculty of Engineering, O.U. AICTE Model Curriculum with effect from Academic Year 2019-20

UNIT IV

Introduction to Mobile app platform for IoT: Protocol stack of Mobile app for IoT, Mobile to server integration, iBeacon in IoS, Window Azure, Linkafy Mobile platform for IoT, Axeda, Xively

UNIT V

Database implementation for IoT: Cloud based IoT platforms, SQL vs NoSQL, Open sourced vs. Licensed Database, Available M2M cloud platform, AxedaXively, Omega NovoTech, Ayla Libellium, CISCO M2M platform, AT &T M2M platform, Google M2M platform, Recent trends in home automation, IoT-locks, Energy optimization in home

Suggested Reading:

1. Olivier Hersent, David Boswarthick, Omar Elloumi, The Internet of Things: Key Applications and Protocols, Wiley-Blackwell.

Course Code			Core/Elective				
PE 3216 ES	W	ireless S	Elective				
Prerequisite	С	ontact Hou	ırs per We	ek	CIF	SEE	Credits
Trerequisite	L	Т	D	Р	CIL	SEE	Credits
-	3	-	3				

- Understand basic sensor network concepts
- > Know physical layer issues, understand and analyse Medium Access Control Protocols
- Comprehend network and transport layer characteristics and protocols and implement conventional protocols
- > Understand the network management and Middleware services
- > Understand the protocols for network security.

Course Outcomes

After completing this course, the student will be able to:

- 1. Present applications of wireless sensors and communication deployment mechanisms
- 2. Describe MAC layer protocols
- 3. Carry out reliable data transfer protocols and routing algorithms in given networks and mechanisms for security
- 4. Understand IoT protocols
- 5. Handle routing management, network security and attacks in OS

UNIT I

Fundamentals of Sensor Networks: Introduction to computer and wireless sensor networks and Overview of the syllabus- Motivation for a network of Wireless Sensor nodes- Sensing and sensors-challenges and constraints - node architecture-sensing subsystem, processor subsystem communication interfaces-prototypes, Application of Wireless sensors- Introduction of Tiny OS Programming and TOSSIM Simulator.

UNIT II

Communication Characteristics and Deployment Mechanisms: Wireless Transmission Technology and Systems-Radio Technology Primer-Available Wireless Technologies - Hardware- Telosb, Micaz motes-Time Synchronization- Clock and the Synchronization Problem - Basics of time Synchronization-Time synchronization protocols - Localization- Ranging Techniques- Range based Localization-Range Free Localization- Event driven Localization

UNIT III

MAC Layer: Overview-Wireless Mac Protocols-Characteristics of MAC protocols in Sensor networks – Contention free MAC Protocols- characteristics- Traffic Adaptive Medium Access-Y-MAC, Low energy Adaptive Clustering - Contention based MAC Protocols- Power Aware Multi-Access with signalling, Sensor MAC-Timeout MAC-Data gathering MAC- Case study –Implementation and Analysis of MAC player protocol in TinyOS

UNIT IV

Routing in Wireless Sensor Networks: Design Issues in WSN routing- Data Dissemination and Gathering-Routing Challenges in WSN - Flooding-Flat Based Routing – SAR, Directed Diffusion, Hierarchical Routing- LEACH, PEGASIS - Query Based Routing- Negotiation Based Routing- Geographical Based Routing- Transport layer- Transport Protocol Design issues- Performance of Transport Control Protocols. Case study- Implementation and analysis of Routing protocol or transport layer protocol in Tiny OS

UNIT V

Middleware and Security Issues: WSN middleware Principles-Middleware Architecture-Existing middleware - operating systems for wireless sensor networks-performance and traffic management - Fundamentals of network security-challenges and attacks - Protocols and mechanisms for security. Case study- Handling attacks in Tiny OS

- 1. Waltenegus Dargie, Christian Poellabauer, "Fundamentals of Wireless Sensor Networks, Theory and Practice", Wiley Series on wireless Communication and Mobile Computing, 2011
- 2. Kazem Sohraby, Daniel Manoli, "Wireless Sensor networks- Technology, Protocols and Applications", Wiley InterScience Publications 2010.
- 3. Bhaskar Krishnamachari, "Networking Wireless Sensors", Cambridge University Press, 2005
- 4. C.S Raghavendra, Krishna M. Sivalingam, Taiebznati, "Wireless Sensor Networks", Springer Science 2004.

Course Code			Core/Elective				
PE 3217 ES		Advance	Elective				
Prerequisite	C	ontact Hou	ırs per We	ek	CIF	SEE	Credits
Trerequisite	L	Т	D	Р	CIL	SEE	Credits
-	3	-	-	-	30	70	3

- The students should get familiar with the wireless/mobile market and the future needs and challenges.
- To get familiar with key concepts of wireless networks, standards, technologies and their basic operations
- > To learn how to design and analyse various medium access
- > To learn how to evaluate MAC and network protocols using network simulation software tools
- > The students should get familiar with the wireless/mobile market and the future needs and challenges

Course Outcomes

After completing this course, the student will be able to:

- 1. Demonstrate advanced knowledge of networking and wireless networking and understand various types of wireless networks, standards, operations and use cases.
- 2. design WLAN, WPAN, WWAN, Cellular based upon underlying propagation and performance analysis.
- 3. Develop mobile applications to solve some of the real-world problems.
- 4. Work with IoT communication techniques
- 5. Understand Vehicular Adhoc Networks

UNIT I

Introduction: Wireless Networking Trends, Key Wireless Physical Layer Concepts, Multiple Access Technologies -CDMA, FDMA, TDMA, Spread Spectrum technologies, Frequency reuse, Radio Propagation and Modelling, Challenges in Mobile Computing: Resource poorness, Bandwidth, energy etc.

Wireless Local Area Networks: IEEE 802.11 Wireless LANs Physical & MAC layer, 802.11 MAC Modes (DCF & PCF) IEEE 802.11 standards, Architecture & protocols, Infrastructure vs. Adhoc Modes, Hidden Node & Exposed Terminal Problem, Problems, Fading Effects in Indoor and outdoor WLANs, WLAN Deployment issues

UNIT II

Wireless Cellular Networks: 1G and 2G, 2.5G, 3G, and 4G, Mobile IPv4, Mobile IPv6, TCP over Wireless Networks, Cellular architecture, Frequency reuse, Channel assignment strategies, Handoff strategies, Interference and system capacity, Improving coverage and capacity in cellular systems, Spread spectrum Technologies.

UNIT III

WiMAX (Physical layer, Media access control, Mobility and Networking), IEEE 802.22 Wireless Regional Area Networks, IEEE 802.21 Media Independent Handover Overview

Wireless Sensor Networks: Introduction, Application, Physical, MAC layer and Network Layer, Power Management, Tiny OS Overview.

UNIT IV

Wireless PANs: Bluetooth AND ZigBee, Introduction to Wireless Sensors.

UNIT V

Security: Security in wireless Networks Vulnerabilities, Security techniques, Wi-Fi Security, DoS in wireless communication.

Advanced Topics: IEEE 802.11x and IEEE 802.11i standards, Introduction to Vehicular Adhoc Networks

- 1. Schiller J., Mobile Communications, Addison Wesley 2000
- 2. Stallings W., Wireless Communications and Networks, Pearson Education 2005
- 3. Stojmenic Ivan, Handbook of Wireless Networks and Mobile Computing, John Wiley and Sons Inc 2002
- 4. Yi Bing Lin and Imrich Chlamtac, Wireless and Mobile Network Architectures, John Wiley and Sons Inc 2000
- 5. Pandya Raj, Mobile and Personal Communications Systems and Services, PHI 200

Course Code			Core/Elective				
PE 3218 ES		Wi	Elective				
December	C	ontact Hou	ırs per We	ek	CIE	SEE	Credits
Fletequisite	L	Т	D	Р	CIE	SEE	Credits
-	3	-	-	-	30	70	3

- Overview of wireless access technologies, Fixed wireless access networks. Terminal mobility issues regarding wireless access to Internet
- Introduction to various Network topologies, hotspot networks, Communication links: point-to-point, point-to-multipoint, multipoint-to-multipoint.
- To provide an overview of Standards for most frequently used wireless access networks: WPAN, UWB, WLAN, WMAN, WWAN. Network services. Wireless access networks planning, design and installation.
- To get and insight of Wireless networking security issues, Wireless access network exploitation and management, software requirements, link quality control.
- Study Recent trends in wireless networking and various access mechanism, new standards of wireless communication.

Course Outcomes

After completing this course, the student will be able to:

- 1. compare various wireless access technologies
- 2. analyze measurements of wireless access network parameter
- 3. assess security issues in wireless networks
- 4. Solve Wireless networking security issues
- 5. Design Wireless access networks

UNIT I

Necessity for wireless terminals connectivity and networking. Wireless networking advantages and disadvantages, Overview of wireless access technologies. Narrowband and broadband networks, fixed and nomadic networks. Wireless local loop (WLL), Public Switched Telephone Network (PSTN) interfaces.

UNIT II

Fixed wireless access (FWA) networks, frequency bands for different networks. Criterions for frequency bands allocation, Network topologies, hotspot networks. Communication links: point-to-point (PTP), point to-multipoint (PMP), multipoint-to-multipoint (MTM).

UNIT III

Standards for most frequently used wireless access networks: WPAN (802.15, Bluetooth, DECT, IrDA), UWB (Ultra-Wideband), WLAN (802.11, Wi-Fi, HIPERLAN, IrDA), WMAN (802.16, WiMAX,

HIPERMAN, HIPERACCESS), WWAN (802.20), Other technologies for broadband wireless access, Local Multipoint Distribution Service (LMDS), Multichannel Multipoint Distribution Service (MMDS). Ad Hoc networks, Network services. Services types based on carrier frequency and bandwidth.

Faculty of Engineering, O.U.AICTE Model Curriculum with effect from Academic Year 2019-20UNIT IV

Wireless access networks planning, design and installation. Services provision, legislative and technical aspects, Technical and economic factors for network planning: expenses, coverage, link capacity, network complexity and carrier-to-interference ratio (C/I). Base station or access point allocation. Base station and access point equipment. Terminal mobility issues regarding wireless access to Internet. Wireless networking security issues.

UNIT V

Example of laptop or handheld PC wireless connection in real environment. PC wireless interface equipment. Wireless access network exploitation and management, software requirements, link quality control. Business model, wireless network services market, market research and marketing, service providers, wireless data application service providers (WDASP) and their role on public telecommunication services market, billing systems. Recent trends in wireless networking and various access mechanism, new standards of wireless communication.

- 1. M. P. Clark, Wireless Access Networks: Fixed Wireless Access and WLL networks -- Design and Operation, John Wiley & Sons, Chichester
- 2. D. H. Morais, Fixed Broadband Wireless Communications: Principles and Practical Applications, Prentice Hall, Upper Saddle River
- 3. R. Pandya, Introduction to WLLs: Application and Deployment for Fixed and Broadband Services, IEEE Press, Piscataway

Course Code			Core/Elective				
PE 3219 ES	En	nbedded	Elective				
Prerequisite	C	ontact Hou	ırs per We	ek	CIF	SEE	Credits
Terequisite	L	Т	D	Р	CIL	SEE	Credits
-	3	3					

- Able to understand embedded Linux development environment, understand and create Linux BSP for a hardware platform
- > Able to program different embedded storage devices
- Able to understand the Linux model for embedded storage, understand and write various embedded Linux drivers such as serial, I²C, and so on.
- > Able to port applications to embedded Linux and write real time applications in embedded Linux

Course Outcomes

After completing this course, the student will be able to:

- 1. Get familiar with the functions of Linux operating systems
- 2. Writing of device driver programming in Linux
- 3. Understand how to configure, build, install and boot from a kernel
- 4. write various embedded Linux drivers such as serial, I^2C , and so on
- 5. Experience Hard Real-Time Linux

UNIT – I

Introduction: History of Embedded Linux, Embedded Linux versus Desktop Linux, Embedded Linux Distributions, Architecture of Embedded Linux, Linux Kernel Architecture, Linux Start Up Sequence, GNU Cross-p\Platform Tool chain.

UNIT – II

Board Support Package: Inserting BSP in Kernel Build Procedure, Boot Loader Interface, Memory Map, Interrupt Management, PCI Subsystem, Timers, UART, and Power Management.

Embedded Storage: Flash Map, MTD—Memory Technology Device, MTD Architecture, Flash Mapping Drivers, MTD Block and Character devices, Embedded File systems, Optimizing Storage Space.

UNIT – III

Embedded Drivers: Linux Serial Driver, Ethernet Driver, I2C subsystem on Linux, USB Gadgets, Watchdog Timer, and Kernel Modules.

UNIT-IV

Porting Applications: Architectural Comparison, Application Porting Road Map, Programming with Pthreads, Operating System Porting Layer (OSPL), Kernel API Driver.

Unit-V

Real-Time Linux: Linux and Real-Time, Real-Time Programming in Linux, Hard Real-Time Linux.

- 1. Embedded Linux System Design and Development, P. Raghavan, Amol Lad, Sriram Neelakandan, 2006, Auerbach Publications
- 2. Embedded Linux Hardware, Software and Interfacing

Course Code				Core/Elective			
PE 3220 ES		Neura	Elective				
Dronoquisito	C	ontact Hou	irs per We	ek	CIF	SEE	Credits
Trerequisite	L	Т	SEE	Credits			
-	3	-	-	-	30	70	3

- Formulate neural networks
- > Understand Training of neural networks using various algorithms
- ➢ Use of neural networks for pattern recognition
- Learn fuzzy systems, application of fuzzy systems
- > Describe Comparison of fuzzy systems with conventional control system.

Course Outcomes

After completing this course, the student will be able to:

- 1. Analyze the various feedback networks
- 2. Apply genetic algorithms to combinatorial optimization problems
- 3. Apply fuzzy logic and reasoning to handle uncertainty and solve various engineering problems
- 4. Analyze the application of fuzzy logic control to real time systems.
- 5. Analyze the application of fuzzy logic control to real time systems.

UNIT I

Introduction to ANS (Artificial Neural systems) Technology, ANS simulation, Types of Neural Networks: Hopfield, perceptron and related models, Adaline and Madaline: Adaline and the Adaptive Linear Combiner, the Madaline and simulating the Adaline. Essential vector operations, Lateral Inhibition and Sensory Processing.

UNIT II

Probabilistic Models, Fuzzy ARTMAP and Recurrent Networks: Probabilistic Neural Networks, General Regression Neural Networks, Fuzzy ARTMAP, Recurrent Back Propagation Neural Networks, Hybrid Learning Neural Networks: Counter propagation Network, Radial basis Function Networks.

UNIT III

Application of Neural Networks: Design and optimization of Systems: Non-Linear optimization, Inverse design problems, Pattern Recognition Applications: Control Chart Pattern Recognition, Recognition of Machine-Cells in a group technology layout. Complex pattern Recognition tasks: Pattern mapping, Temporal patters, pattern variability, Neocognitron, Addition of lateral inhibition and Feedback to the Neocognitron.

UNIT IV

Introduction to Fuzzy systems, Fuzzy sets and operations on Fuzzy sets, Basics of Fuzzy relations, Fuzzy measures, Fuzzy integrals, Transform Image coding with Adaptive Fuzzy systems, Adaptive FAM systems for Transform coding.

UNIT V

Comparison of Fuzzy and Kalman-Filter Target, Tracking control systems, Fuzzy and Math Model Controllers, Real Time Target Tracking, Fuzzy Controller, Kalmaln-Filter Controller, Fuzzified CMAC and RBF – Network based self-learning Controllers.

- 1. James A. Freeman and David M. Skapura, *Neural Networks: Algorithms, Applications and Programming Techniques*, Pearson Education, India, 2008.
- 2. James A. Anderson, An introduction to Neural Networks, PHI, 2003.
- 3. B. Yegnanarayana, Artificial Neural Networks, PHI Publications India, 2006.
- 4. Timothy J. Ross Fuzzy Logic with Engineering Applications, McGraw Hill 2004.
- 5. Bart Kosko, Neural Networks and Fuzzy Systems, PHI India Publications, 2008.

Course Code			Core/Elective									
PE 3221 ES		P	Elective									
Prerequisite	C	ontact Hou	Credits									
Trerequisite	L T	Т	D	Р		SEL	creatis					
-	3	-	3									
Course Objectives												
Ability to un	nderstand t	rstand the Security requirements in IoT										

- Understand the cryptographic fundamentals for IoT
- > Ability to understand the authentication credentials and access control
- > Understand the various types Trust models and Cloud Security.
- > Understand New directions in cloud enabled IoT computing

Course Outcomes

After completing this course, the student will be able to:

- 1. Identify and analyse IoT security and privacy risks
- 2. Concept design for secure hardware and software
- 3. Analyse the social and privacy impacts of the IoT
- 4. Design self-organising things for security
- 5. Understand New directions in cloud enabled IoT computing

UNIT I

Introduction - Securing The Internet of Things: Security Requirements in IoT Architecture - Security in Enabling Technologies - Security Concerns in IoT Applications. Security Architecture in the Internet of Things - Security Requirements in IoT - Insufficient Authentication/Authorization - Insecure Access Control - Threats to Access Control, Privacy, and Availability - Attacks Specific to IoT. Vulnerabilities –

Secrecy and Secret-Key Capacity - Authentication/Authorization for Smart Devices - Transport Encryption – Attack & Fault trees

UNIT II

Cryptographic Fundamentals for IoT: Cryptographic primitives and its role in IoT – Encryption and Decryption – Hashes – Digital Signatures – Random number generation – Cipher suites – key management fundamentals – cryptographic controls built into IoT messaging and communication protocols – IoT Node Authentication

UNIT III

Identity & Access Management Solutions for IoT: Identity lifecycle – authentication credentials – IoT IAM infrastructure – Authorization with Publish / Subscribe schemes – access control

UNIT IV

Privacy Preservation and Trust Models for IoT: Concerns in data dissemination – Lightweight and robust schemes for Privacy protection – Trust and Trust models for IoT – self-organizing Things - Preventing unauthorized access.

UNIT V

Cloud Security for IoT: Cloud services and IoT – offerings related to IoT from cloud service providers – Cloud IoT security controls – An enterprise IoT cloud security architecture – New directions in cloud enabled IoT computing.

- 1. Practical Internet of Things Security (Kindle Edition) by Brian Russell, Drew Van Duren
- 2. Securing the Internet of Things, Elsevier
- 3. Security and Privacy in Internet of Things (IoTs): Models, Algorithms, and Implementations

Course Code				Core/Elective			
PE 3222 ES		IoT: S	Elective				
Prerequisite	C	ontact Hou	ırs per We	ek	CIF	SEE	Credits
Trerequisite	L	Т	D	Р	CIL	SEE	Credits
-	3	-	-	-	30	70	3

- Understanding of IoT value chain structure (device, data cloud), application areas and technologies involved
- Understand IoT sensors and technological challenges faced by IoT devices, with a focus on wireless, energy, power, RF and sensing modules
- Analyse the technologies in sensors and actuators
- > Market forecast for IoT devices with a focus on sensors
- Explore and learn about Internet of Things with the help of preparing projects designed for Raspberry Pi

Course Outcomes

After completing this course, the student will be able to:

- 1. Develop thinking of making new generations of sensors
- 2. Analyse power management
- 3. Design an IoT project
- 4. Design wearable devices
- 5. Design Prototypes

UNIT I

Introduction: Internet of Things Promises–Definition – Scope–Sensors for IoT, Applications–Structure of IoT– IoT Map Device.

UNIT II

Seven Generations of IoT Sensors to Appear: Industrial sensors – Description & Characteristics–First eneration – Description & Characteristics–Advanced Generation – Description & Characteristics–Integrated IoT Sensors – Description & Characteristics– Polytronics Systems – Description & Characteristics–Sensors' Swarm – Description & Characteristics–Printed Electronics – Description & Characteristics–IoT Generation Roadmap

UNIT III

Technological Analysis: Wireless Sensor Structure–Energy Storage Module–Power Management Module– RF Module–Sensing Module

UNIT IV

IoT Development Examples: ACOEM Eagle – EnOcean Push Button – NEST Sensor – Ninja Blocks Focus on Wearable Electronics

UNIT V

Preparing IoT Projects: Creating the sensor project - Preparing Raspberry Pi - Clayster libraries - Hardware- Interacting with the hardware - Interfacing the hardware- Internal representation of sensor values - Persisting data - External representation of sensor values - Exporting sensor data - Creating the hardware - Creating a controller - Representing sensor values - Parsing sensor data - Calculating control states - Creating a camera - Hardware - Accessing the serial port on

Faculty of Engineering, O.U. AICTE Model Curriculum with effect from Academic Year 2019-20

Raspberry Pi - Interfacing the hardware - Creating persistent default settings - Adding configurable properties - Persisting the settings - Working with the current settings Initializing the camera

- Dr. Guillaume Girardin, Antoine Bonnabel, Dr. Eric Mounier, 'Technologies & Sensors for the Internet of Things Businesses & Market Trends 2014 - 2024', Yole Développement Copyrights ,2014
- 2. Peter Waher, 'Learning Internet of Things', Packt Publishing, 2015
- 3. Editors Ovidiu Vermesan Peter Friess, Internet of Things From Research and Innovation to Market
- 4. Deployment', River Publishers, 2014
- 5. N. Ida, Sensors, Actuators and Their Interfaces, Scitech Publishers, 2014.

Course Code				Core/Elective			
PE 3223 ES	E	nergy Ha Ma	Elective				
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Trerequisite	L	Т	SEE	Credits			
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- > Understand the various energy sources and energy harvesting based sensor networks
- > Learn about the various Piezoelectric materials and Non-linear techniques
- > Learn principles of Electromagnetic Energy Harvesting and Non-Linear Techniques
- Understand the various Power sources for WSN
- > Learn about the applications of Energy harvesting systems.

Course Outcomes

After completing this course, the student will be able to:

- 1. Develop Novel energy harvesting hardware, devices, systems
- 2. Design Self-sustaining wearable devices
- 3. Recognise alternative power sources for embedded applications, such as novel nuclear, chemical, or biological for energy harvesting
- 4. Design Bio-MEMS based applications

UNIT I

Energy Harvesting Systems: Introduction – Energy sources – energy harvesting based sensor networks – photovoltaic cell technologies – generation of electric power in semiconductor PV cells – types

UNIT II

Piezo-Electric Energy Harvesting and Electromechanical Modelling: Piezoelectric materials – transducers – harvesters – microgenerators – strategies for enhancing the performance of energy harvesters. Electromechanical modelling of Lumped parameter model and coupled distributed parameter models and closed-form solutions

UNIT III

Electromagnetic Energy Harvesting and Non-Linear Techniques: Basic principles – micro fabricated coils and magnetic materials – scaling – power maximations – micro and macro scale implementations. Non-linear techniques – vibration control & steady state cases

UNIT IV

Energy Harvesting Wireless Sensors: Power sources for WSN – Power generation – conversion – examples – case studies. Harvesting microelectronic circuits – power conditioning and losses.

UNIT V

Selected Applications of Energy Harvesting Systems: Case studies for Implanted medical devices – Bio-MEMS based applications – harvesting for RF sensors and ID tags – powering wireless SHM sensor nodes.

- 1. Carlos Manuel Ferreira Carvalho, Nuno Filipe Silva Veríssimo Paulino, "CMOS Indoor Light Energy Harvesting System for Wireless Sensing Applications", Springer
- 2. Danick Briand, Eric Yeatman, Shad Roundy, "Micro Energy Harvesting"

Course Code				Core/Elective			
PE 3224 ES			Elective				
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- > The principles of scripting languages.
- > Motivation for and applications of scripting.
- > Difference between scripting languages and non- scripting languages.
- > Types of scripting languages.
- Scripting languages such as PERL, TCL/TK, python and BASH.

Course Outcomes

After completing this course, the student will be able to:

- 1. Demonstrate knowledge and understanding of the nature of scripting and the role of scripting languages
- 2. Write simple scripts to automate system administration tasks using appropriate languages
- 3. Develop simple applications using appropriate tools.
- 4. Creation of programs in the Linux environment.
- 5. Usage of scripting languages in IC design flow.

UNIT I

Linux Basics: Introduction to Linux, File System of the Linux, General usage of Linux kernel & basic commands, Linux users and group, Permissions for file, directory and users, searching a file & directory, zipping and unzipping concepts

UNIT II

Linux Networking Introduction to Networking in Linux, Network basics & Tools, File Transfer Protocol in Linux, Network file system, Domain Naming Services, Dynamic hosting configuration Protocol & Network information Services.

UNIT III

Perl Scripting: Introduction to Perl Scripting, working with simple values, Lists and Hashes, Loops and Decisions, Regular Expressions, Files and Data in Perl Scripting, References & Subroutines, Running and Debugging Perl, Modules, Object – Oriented Perl.

UNIT IV

Tcl / Tk Scripting: Tcl Fundamentals, String and Pattern Matching, Tcl Data Structures, Control Flow Commands, Procedures and Scope, Evel, Working with Unix, Reflection and Debugging, Script Libraries, Tk Fundamentals, Tk by examples, The Pack Geometry Manager, Binding Commands to X Events, Buttons and Menus, Simple Tk Widgets, Entry and List Box Widgets Focus, Grabs and Dialogs.

UNIT V

Python Scripting: Introduction to Python, using the Python Interpreter, More Control Flow Tools, Data Structures, Modules, Input and Output, Errors and Exceptions, Classes, Brief Tour of the Standard Library.

- 1. Learning Python -2^{nd} Ed., Mark Lutz and David Ascher, 2003, O'Reilly.
- 2. Perl in 24 Hours 3rd Ed., Clinton Pierce, 2005, Sams Publishing.
- 3. Learning Perl 4th Ed. Randal Schwartz, Tom Phoenix and Brain d foy. 2005.
- 4. Jython Essentials Samuele Pedroni and Noel Pappin.2002. O'Reilly.
- Programming Perl Larry Wall, Tom Christiansen and John Orwant, 3rd Edition, O'Reilly, 2000. (ISBN 0596000278)
- 6. Python Tutorial by Guido Van Rossum, Fred L. Drake Jr. editor, Release 2.6.4
- 7. Practical Programming in Tcl and Tk by Brent Welch, Updated for Tcl 7.4 and Tk 4.0.
- 8. Teach Yorself Perl in 21 days by David Till.
- 9. Red Hat Enterprise Linux 4: System Administration Guide Copyright, 2005 Red Hat Inc.

Course Code				Core/Elective			
PE 3225 ES		In	Elective				
Prerequisite	C	ontact Hou	ırs per We	ek	CIE	SEE	Credits
Trerequisite	L	Т	D	Р			Creatis
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- > Study fundamental concepts of Image Processing and various Image Transforms
- Learn Image Enhancement Techniques in Spatial and Frequency domain, Image Segmentation methods
- > Familiarize with fundamentals of Image compression, Lossy & Lossless Compression methods.
- > Define concepts of Video Processing, Image Formation models, and processing of Video signals.
- > Understand general methodologies of 2 D Motion Estimation and Video coding methods.

Course Outcomes

After completing this course, the student will be able to:

- 1. Use different transforms for various applications like Image Enhancement, Compression etc.
- 2. Use Spatial and Transform techniques to Enhance the given image and to extract the features of the image.
- 3. Use Lossless and Lossy compression techniques for real time applications.
- 4. Understand the fundamental concepts of Video capturing and Three Dimensional Motion Models.
- 5. Understand and analyse various Motion estimation techniques.

UNIT I

Fundamentals of Image Processing and Image Transforms: Basic steps of Image Processing System, Sampling and Quantization of an image, relationship between pixels

Image Transforms: 2 D- Discrete Fourier Transform, Discrete Cosine Transform (DCT), Wavelet Transforms: Continuous Wavelet Transform, Discrete Wavelet Transforms.

UNIT II

Image Processing Techniques Image Enhancement: Spatial domain methods: Histogram processing, Fundamentals of Spatial filtering, smoothing spatial filters, Sharpening spatial filters.

Frequency domain methods: Basics of filtering in frequency domain, image smoothing, image sharpening, Selective filtering. Laplacian of Gaussian (LOG) filters.

Image Segmentation: Segmentation concepts, Point, Line and Edge Detection, Thresholding, Region

UNIT III

Image Compression: Image compression fundamentals - Coding Redundancy, Spatial and Temporal redundancy, Compression models: Lossy & Lossless, Huffman coding, Arithmetic coding, LZW coding, Run length coding, Bit plane coding, Transform coding, Predictive coding, Wavelet coding, JPEG Standards.

UNIT IV

Basic concepts of Video Processing: Analog Video, Digital Video. Time-Varying Image Formation models: Three-Dimensional Motion Models, Geometric Image Formation, Photometric Image Formation, Sampling of Video signals, Filtering operations.

UNIT V

Motion Estimation: Optical flow, General Methodologies, Pixel Based Motion Estimation, Block-Matching Algorithm, Mesh based Motion Estimation, Global Motion Estimation, Region based Motion Estimation, Multi resolution motion estimation, Waveform based coding, Block based transform coding, Predictive coding, Application of motion estimation in Video coding, constant dependent video coding and joint shape and texture coding .MPEG and H.26X standards.

- 1. Gonzaleze and Woods, *Digital Image Processing*, 3rd edition, Pearson.
- 2. Yao Wang, Joem Ostermann, Ya–quin Zhang, *Video processing and communication*, 1st Edition, PH Int.
- 3. S. Jayaraman, S.Esakkirajan, T.Veera Kumar Digital Image Processing, TMH, 2009.
- 4. M. Tekalp, Digital *Video Processing*, Prentice Hall International
- 5. John Woods, *Multi-dimensional Signal, Image and Video Processing and Coding* 2nd Edition, Elsevier.
- 6. Vipula Singh, Digital Image Processing with MATLAB and LabVIEW, Elsevier, 2013
- 7. Keith Jack, *Video Demystified A Hand Book for the Digital Engineer*, 5th Edition, Elsevier.

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3	-	3				
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- > To learn the fundamental of device driver and write simple device driver programs
- > To learn the debugging technique and study the concurrency and Trace conditions
- > To learn about the interrupt handling, PCI driver and USB driver
- > To learn the block driver and network driver

Course Outcomes

After completing this course, the student will be able to:

- 1. write simple device driver programs
- 2. Handle interrupts and drivers
- 3. Measure the performance of your device driver.
- 4. write programs for devices in Linux.
- 5. Design and implement a kernel module.

UNIT I

Introduction to Device Driver and Char Driver: Introduction to device driver - The Role of the Device Driver –Splitting the Kernel - Classes of Devices and Modules - Security Issues – Building and running modules – Setting your test system – compiling and loading - Char Drivers - Design of scull - Some Important Data Structures - Char Device Registration - open and release - scull's Memory Usage - read and write - Playing with the New Devices

UNIT II

Debugging Technique, Concurrency and Trace Conditions: Debugging technique -Concurrency and trace conditions – Pitfalls in scull - Concurrency and Its management - Semaphores and Mutexes - Completions – Spinlocks - Locking Traps - Alternatives to Locking - Advanced Char driver operations – ioctl 135 - Blocking I/O 147 - poll and select 163 - Asynchronous Notification - Seeking a Device - Access Control on a Device File

UNIT III

Memory Allocation, Communicating with Hardware: Time, delays and deferred work – Allocating memory – The Real Story of kmalloc - Lookaside Caches - get_free_page and Friends - vmalloc and Friends - Per-CPU Variables - Obtaining Large Buffers - Communicating with hardware – I/O Ports and I/O Memory - Using I/O Ports - I/O Port Example - Using I/O Memory

UNIT IV

Interrupt Handling, Data Types, Pci Driver and Usb Driver: Interrupt handling - Preparing the Parallel Port - Installing an Interrupt Handler - Implementing a Handler - Top and Bottom Halves - Interrupt Sharing - Interrupt-Driven I/O - Data types in kernel – Use of Standard C Types - Assigning an Explicit Size to Data Items - Interface-Specific Types - Other Portability Issues - Linked Lists - PCI drivers - PCI Interface - PC/104 and PC/104+ - Other PC Buses - USB drivers - USB and Sysfs - USB Urbs - Writing a USB Driver - USB Transfers Without Urbs

UNIT V

LINUX Device Model, Block Driver and Network Drivers: Linux device model - Kobjects, Ksets, and Subsystems - Low-Level Sysfs Operations - Hotplug Event Generation - Buses, Devices, and Drivers – Classes – Hotplug - Block Driver – Registration - Block Device Operations - Request Processing - Network Drivers

- 1. Robert love "Linux Kernel Development" Pearson Publication, Third edition 2010
- 2. Beck Michael et al "Linux Kernel Programming" Pearson Publication, Third edition 2015
- 3. Mohan LalJangir "Linux kernel and device driver programming", Laxmi Publication, 2014

Course Code			Core/Elective				
PE 3227 ES			Elective				
Prerequisite	Contact Hours per Week					SEE	Credits
Trerequisite	L	Т	D	Р	CIL	SEE	Credits
-	3	-	-	70	3		

- > The student will also learn how to apply trust-based security model to real-world security problems
- An overview of the concepts, processes, and best practices needed to successfully secure information within Cloud infrastructures
- > Students will learn the basic Cloud types and delivery models
- Develop an understanding of the risk and compliance responsibilities and Challenges for each Cloud type and service delivery model
- > Understand Security Management in the Cloud and Privacy Issues

Course Outcomes

After completing this course, the student will be able to:

- 1. Identify security aspects of each cloud model
- 2. Develop a risk-management strategy for moving to the Cloud
- 3. Implement a public cloud instance using a public cloud service provider
- 4. Apply trust-based security model to different layer
- 5. Apply techniques for Security Management in the Cloud and Privacy Issues

UNIT I

Introduction to Cloud Computing: Online Social Networks and Applications, Cloud introduction and overview, Different clouds, Risks, Novel applications of cloud computing

UNIT II

Cloud Computing Architecture: Requirements, Introduction Cloud computing architecture, On Demand Computing Virtualization at the infrastructure level, Security in Cloud computing environments, CPU Virtualization, A discussion on Hypervisors Storage Virtualization Cloud Computing Defined, The SPI Framework for Cloud Computing, The Traditional Software Model, The Cloud Services Delivery Model **Cloud Deployment Models:** Key Drivers to Adopting the Cloud, The Impact of Cloud Computing on Users, Governance in the Cloud, Barriers to Cloud Computing Adoption in the Enterprise

UNIT III

Security Issues in Cloud Computing: Infrastructure Security, Infrastructure Security: The Network Level, The Host Level, The Application Level, Data Security and Storage, Aspects of Data Security, Data Security Mitigation Provider Data and Its Security

Identity and Access Management: Trust Boundaries and IAM, IAM Challenges, Relevant IAM Standards and Protocols for Cloud Services, IAM Practices in the Cloud, Cloud Authorization Management

UNIT IV

Security Management in the Cloud: Security Management Standards, Security Management in the Cloud, Availability Management: SaaS, PaaS, IaaS

Privacy Issues: Privacy Issues, Data Life Cycle, Key Privacy Concerns in the Cloud, Protecting Privacy, Changes to Privacy Risk Management and Compliance in Relation to Cloud Computing, Legal and Regulatory Implications, U.S. Laws and Regulations, International Laws and Regulations

UNIT V

Audit and Compliance: Internal Policy Compliance, Governance, Risk, and Compliance (GRC), Regulatory/External Compliance, Cloud Security Alliance, Auditing the Cloud for Compliance, Security-as-a-Cloud

- 1. Cloud Computing Explained: Implementation Handbook for Enterprises, John Rhoton, Publication Date: November 2, 2009
- 2. Cloud Security and Privacy: An Enterprise Perspective on Risks and Compliance (Theory in Practice), Tim Mather, ISBN-10: 0596802765, O'Reilly Media, September 2009

Course Code				Core/Elective			
PE 3228 ES			Elective				
Prerequisite	C	ontact Hou	ırs per We	ek	CIE	SEE	Credits
Terequisite	L	Т	D	Р			
-	3	-	-	-	30	70	3

- > Learn front end devices for information access and their operating systems.
- > Familiarize with Communication between the mobile equipment and the base station transceiver
- > Learn transmission and reception of data directory service
- Formulate network routing
- Estimate transaction.

Course Outcomes

After completing this course, the student will be able to:

- 1. Explain the principles and theories of mobile computing technologies.
- 2. Describe the possible future of mobile computing technologies and applications.
- 3. Design effective mobile interfaces using human computer interaction principles.
- 4. Understand different Ad hoc Network Routing protocols
- 5. Estimate transaction.

UNIT I

Introduction: Challenges in mobile computing, coping with uncertainties, Resource poorness, bandwidth, etc. Cellular architecture, co-channel interference, frequency reuse, capacity increase by cell splitting: Evolution of mobile system: CDMA, FDMA, TDMA and GSM.

UNIT II

Mobility Management: Cellular architecture, Co-channel interference, Mobility: handoff, types of handoffs; Location management, HLR-VLR scheme, Hierarchial scheme, Predictive location management schemes, Mobile IP, Cellular IP.

UNIT III

Publishing and Accessing Data in Air: Pull and Push based data delivery models, Data dissemination by broadcast, Broadcast disks, Directory service in air, energy efficient indexing scheme for push based data delivery.

File system support for mobility: Distributed file sharing for mobility support, Coda and other storage manager for mobility support.

UNIT IV

Ad hoc Network Routing protocols: Ad hoc network routing protocols, destination sequenced distance vector algorithm, Cluster based gate way switch routing, Global state routing, fish- eye state routing, dynamic source routing, ad hoc on-demand routing, location aided routing, Zonal routing algorithm.

UNIT V

Mobile Transaction and Commerce: Models for mobile transaction, Kangaroo and Joey transactions, Team transaction, Recovery model for mobile transactions. Electronic payment and protocols for mobile commerce.

- 1. Jochen Schiller, *Mobile Communications*, 2nd edition, Pearson Education, 2004.
- 2. Hansmann, Merk, Nicklous, Stober, *Principles of mobile Computing*, 2nd edition, Springer International Edition, 2003.
- 3. *A Survey of Mobile transactions appeared in distributed and parallel data bases*, 16, 193-230, 2004, Kluwer Academic Publishers.
- 4. S. Acharya, M. Franklin and S. Zdonik, *Balancing Push and pull for Data Broadcast*, Proceedings of the ACM SIGMOD, Tuscon, AZ, May 1997.

Course Code				Core/Elective			
PE 3319 EV			Elective				
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rielequisite	L	Т	D	Р	CIE	SEE	Credits
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- > Understand Integration of hardware and software on a single chip
- Describe various processors
- Design of Memory for SoC
- > Familiarize with Interconnection of various devices and reconfiguration
- > Explore various application of system on single chip

Course Outcomes

After completing this course, the student will be able to:

- 1. Apply fundamental knowledge of digital logic design to modelling and analysis of low power in processor design.
- 2. Understand the design concepts of processor, pipelining concepts, ARM Development Tools and Interfacing ARM with Co-processors.
- 3. Understand the concepts of Memory Hierarchy, Cache design and Memory Management.
- 4. Develop an understanding of various interconnect schemes for system Development.
- 5. Design a simple SoC for reconfigurability/low power/ASIP/NISC etc.

UNIT I

Introduction to the System Approach: System Architecture, Components of the system, Hardware & Software, Processor Architectures, Memory and Addressing. System level interconnection, An approach for SOC Design, System Architecture and Complexity.

UNIT II

Processors: Introduction, Processor Selection for SOC, Basic concepts in Processor Architecture, Basic concepts in Processor Micro Architecture, Basic elements in Instruction handling. Buffers: minimizing Pipeline Delays, Branches, More Robust Processors, Vector Processors and Vector Instructions extensions, VLIW Processors, Superscalar Processors.

UNIT III

Memory Design for SOC: Overview of SOC external memory, Internal Memory, Size, Scratchpads and Cache memory, Cache Organization, Cache data, Write Policies, Strategies for line replacement at miss time, Types of Cache, Split – I, and D – Caches, Multilevel Caches, Virtual to real translation, SOC Memory System, Models of Simple Processor – memory interaction.

UNIT IV

Interconnect Customization and Configuration: Inter Connect Architectures, Bus: Basic Architectures, SOC Standard Buses, Analytic Bus Models, Using the Bus model, Effects of Bus transactions and contention time. SOC Customization: An overview, Customizing Instruction Processor, Reconfiguration Technologies, Mapping design onto Reconfigurable devices, Instance- Specific design, Customizable Soft Processor, Reconfiguration - overhead analysis and trade-off analysis on reconfigurable Parallelism.

UNIT V

Application Studies / Case Studies: SOC Design approach, AES algorithms, Design and evaluation, Image compression – JPEG compression.

- 1. Ricardo Reis, "Design of System on a Chip: Devices and Components, 1st Ed., Springer, 2004.
- 2. Michael J. Flynn and Wayne Luk, Computer System Design System-on-Chip, Wiley India Pvt. Ltd.
- 3. Steve Furber, *ARM System on Chip Architecture*, 2nd Ed., Addison Wesley Professional, 2000.
- 4. Jason Andrews, *Co-Verification of Hardware and Software for ARM System on Chip Design* (*Embedded Technology*), Newnes, BK and CDROM.
- 5. Prakash Rashinkar, Peter Paerson and Leena Singh L, *System on Chip Verification Methodologies and Techniques*, Kluwer Academic Publishers, 2001.

Course Code				Core/Elective			
PC 3303 EV		Rea	Elective				
Prerequisite	C	ontact Hou	ırs per We	æk	CIE	SEE	Credits
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- Understand concepts of OS and RTOS
- Describe UNIX OS
- Distinguish between Hard and Soft RTOS
- > Analyse the concept of Embedded RTOS
- Explore VxWorks.

Course Outcomes

After completing this course, the student will be able to:

- 1. Describe the features of UNIX operating system and differentiate between UNIX and POSIX.
- 2. Differentiate between Hard and Soft Real time systems and familiarize with classical Uni-processor scheduling algorithms
- 3. Understand the concepts of Real time operating systems and analyse the Inter process communication.
- 4. Explain the features of VxWorks and compare the commercially available RTOS's
- 5. Understand the debugging tools and cross development environment.

UNIT I

Brief Review of Unix Operating Systems (Unix Kernel – File system, Concepts of – Process, Concurrent Execution & Interrupts. Process Management – forks & execution. Programming with system calls, Process Scheduling. Shell programming and filters).

Portable Operating System Interface (POSIX) – IEEE Standard 1003.13 & POSIX real time profile. POSIX versus traditional Unix signals, overheads and timing predictability.

UNIT II

Hard versus Soft Real-time systems – examples, Jobs & Processors, Hard and Soft timing constraints, Hard Real-time systems, Soft Real-time systems. Classical Uni-processor Scheduling Algorithms – RMS, Preemptive EDF, Allowing for Preemptive and Exclusion Condition.

UNIT III

Concept of Embedded Operating Systems, Differences between Traditional OS and RTOS. Real time System Concepts, RTOS Kernel & Issues in Multitasking – Task Assignment, Task Priorities, Scheduling, Inter task Communication & Synchronization – Definition of Context Switching, Foreground ISRs and Background Tasks. Critical Section – Reentrant Functions, Inter process Communication (IPC) – IPC through Semaphores, Mutex, Mailboxes, Message Queues or Pipes and Event Flags.

UNIT IV

 $\label{eq:VxWorks-POSIX} \begin{array}{l} \mbox{Real Time Extensions, timeout features, Task Creation, Semaphores (Binary, Counting),} \\ \mbox{Mutex, Mailbox, Message Queues, Memory Management} - Virtual to Physical Address Mapping.} \\ \mbox{Comparison of RTOS} - VxWorks, \mbox{μC/OS-II} and RT Linux for Embedded Applications.} \end{array}$

Faculty of Engineering, O.U. AICTE Model Curriculum with effect from Academic Year 2019-20

UNIT V

Debugging Tools and Cross Development Environment – Software Logic Analyzers, ICEs. Comparison of RTOS – VxWorks, μ C/OS-II and RT Linux for Embedded Applications.

- 1. Jane W.S.Liu, *Real Time Systems*, Pearson Education, Asia, 2001.
- 2. Wind River Systems, VxWorks Programmers Guide, Wind River Systems Inc. 1997.
- 3. Shibu K.V., Introduction to embedded systems, MC Graw-Hill Inc., 1997.
- 4. Tanenbaum, *Modern Operating Systems*, 3rd edition, Pearson Edition, 2007.
- 5. Jean.J. Labrosse, *MicroC/OS-II*, The CMP Books.
- 6. C.M. Krishna and G. Shin, *Real Time System*, McGraw Hill International Editions, 1997.

Course Code				Core/Elective				
MC 5161 ME		Rese	Mandatory Course					
Draraquisita	Contact Hours per Week					Credits		
Trerequisite	L	Т	Credits					
-	3	3 30 70						

To make students to

- Motivate to choose research as career
- > Formulate the research problem, prepare the research design
- > Identify various sources for literature review and data collection report writing
- > Equip with good methods to analyse the collected data
- Know about IPR copyrights

Course Outcomes

At the end of this course, students will be able to:

- 1. Define research problem, review and asses the quality of literature from various sources
- 2. Improve the style and format of writing a report for technical paper/ Journal report, understand and develop various research designs
- 3. Collect the data by various methods: observation, interview, questionnaires
- 4. Analyse problem by statistical techniques: ANOVA, F-test, Chi-square
- 5. Understand apply for patent and copyrights

UNIT - I

Research Methodology: Objectives and Motivation of Research, Types of Research, research approaches, Significance of Research, Research Methods Verses Methodology, Research Process, Criteria of Good Research, Problems Encountered by Researchers in India, Benefits to the society in general. Defining the Research Problem: Selection of Research Problem, Necessity of Defining the Problem

UNIT - II

Literature Survey and Report writing: Importance and purpose of Literature Survey, Sources of Information, Assessment of Quality of Journals and Articles, Need of Review, Guidelines for Review, Record of Research Review.

Report writing: Meaning of interpretation, layout of research report, Types of reports, Mechanism of writing a report. **Research Proposal Preparation**: Writing a Research Proposal and Research Report, Writing Research Grant Proposal.

UNIT - III

Research Design: Meaning of Research Design, Need of Research Design, Feature of a Good Design, Important Concepts Related to Research Design, Different Research Designs, Basic Principles of Experimental Design, Developing a Research Plan, Steps in sample design, types of sample designs.

UNIT - IV

Data Collection and Analysis: Methods of data collection, Data organization, Methods of data grouping, Diagrammatic representation of data, Graphic representation of data. Importance of Parametric, non-parametric test, testing of variance of two normal populations, use of Chi-square, ANOVA, F-test, z-test

Faculty of Engineering, O.U.AICTE Model Curriculum with effect from Academic Year 2019-20UNIT - V

Intellectual Property Rights: Meaning, Nature, Classification and protection of Intellectual Property, The main forms of Intellectual Property, Concept of Patent, Patent document, Invention protection, Granting of patent, Rights of a patent, Licensing, Transfer of technology.

- 1. C.R Kothari, Research Methodology, Methods & Techniquel; New Age International Publishers, 2004
- 2. R. Ganesan, Research Methodology for Engineersl, MJP Publishers, 2011
- 3. Y.P. Agarwal, Statistical Methods: Concepts, Application and Computation^{II}, Sterling Publications Pvt. Ltd., New Delhi, 2004
- 4. G.B. Reddy, Intellectual Property Rights and the Law 5th Ed. 2005 Gogia Law Agency
- 5. Ajit Parulekar and Sarita D'Souza, Indian Patents Law Legal & Business Implications, Macmillan India Ltd, 2006

Course Code			Core/Elective						
OE 9101 CE		Cost Mar	Open Elective						
Durana anticita	C	ontact Hou	ırs per We	æk	CIE	SEE	Credits		
Trerequisite	L	Т	Credits						
-	3	3 30 70							

- > To apply modern software packages to conduct analysis of real world data.
- > To understand the technical underpinning of engineering economic analysis.
- The ability to apply the appropriate analytical techniques to a wide variety of real world problems and data sets.
- > To summarize and present the analysis results in a clear and coherent manner.

Course Outcomes

At the end of this course, students will be able to:

- 1. Students should be able to learn the cost concepts in decision making
- 2. Student should be able to do cost planning and Marginal Costing
- 3. Students should be able to create a database for operational control and decision making.

UNIT-I

Introduction and Overview of the Strategic Cost Management Process: Cost concepts in decisionmaking; Relevant cost, Differential cost, Incremental cost and Opportunity cost. Objectives of a Costing System; Inventory valuation; Creation of a Database for operational control; Provision of data for Decision-Making.

UNIT-II

Project: meaning, Different types, why to manage, cost overruns centers, various stages of project execution: conception to commissioning. Project execution as conglomeration of technical and non-technical activities. Detailed Engineering activities. Pre project execution main clearances and documents Project team: Role of each member. Importance Project site: Data required with significance. Project contracts. Types and contents. Project execution Project cost control. Bar charts and Network diagram. Project commissioning: mechanical and process.

UNIT-III

Cost Behavior and Profit Planning Marginal Costing: Distinction between Marginal Costing and Absorption Costing; Break-even Analysis, Cost-Volume-Profit Analysis. Various decision-making problems. Standard Costing and Variance Analysis.

Pricing strategies: Pareto Analysis. Target costing, Life Cycle Costing. Costing of service sector. Just-intime approach, Material Requirement Planning, Enterprise Resource Planning, Total Quality Management and Theory of constraints.

UNIT-IV

Activity-Based Cost Management: Bench Marking; Balanced Score Card and Value-Chain Analysis. Budgetary Control; Flexible Budgets; Performance budgets; Zero-based budgets. Measurement of Divisional profitability pricing decisions including transfer pricing.

UNIT-V

Quantitative techniques for cost management: Linear Programming, PERT/CPM, Transportation problems, Assignment problems, Simulation, Learning Curve Theory.

- 1. Cost Accounting A Managerial Emphasis, Prentice Hall of India, New Delhi
- 2. Charles T. Horngren and George Foster, Advanced Management Accounting
- 3. Robert S Kaplan Anthony A. Alkinson, Management & Cost Accounting

Course Code			Core/Elective				
OE 9102 CS			Open Elective				
Durana aviaita	C	ontact Hou	ırs per We	ek	CIE	SEE	Credits
Trerequisite	L	Т	D	Р	CIL	SEE	Credits
-	3	-	-	-	30	70	3

- > Understand the role of business analytics within an organization
- Analyse data using statistical and data mining techniques and understand relationships between the underlying business processes of an organization
- To gain an understanding of how managers use business analytics to formulate and solve business problems and to support managerial decision making
- > To become familiar with processes needed to develop, report, and analyse business data
- Use decision-making tools/Operations research techniques
- > Mange business process using analytical and management tools
- Analyse and solve problems from different industries such as manufacturing, service, retail, software, banking and finance, sports, pharmaceutical, aerospace etc.
- Student will able to understand the basic rules of research formulation and procedure for obtaining patent rights

Course Outcomes

At the end of this course, students will be able to:

- 1. Students will demonstrate knowledge of data analytics
- 2. Students will demonstrate the ability of think critically in making decisions based on data and deep analytics
- 3. Students will demonstrate the ability to use technical skills in predicative and prescriptive modelling to support business decision-making
- 4. Students will demonstrate the ability to translate data into clear, actionable insights

UNIT-I

Business analytics: Overview of Business analytics, Scope of Business analytics, Business Analytics Process, Relationship of Business Analytics Process and organization, competitive advantages of Business Analytics.

Statistical Tools: Statistical Notation, Descriptive Statistical methods, Review of probability distribution and data modelling, sampling and estimation methods overview.

UNIT-II

Trendiness and Regression Analysis: Modelling Relationships and Trends in Data, simple Linear Regression. Important Resources, Business Analytics Personnel, Data and models for Business analytics, problem solving, Visualizing and Exploring Data, Business Analytics Technology.

UNIT-III

Organization Structures of Business analytics, Team management, Management Issues, Designing Information Policy, Outsourcing, Ensuring Data Quality, measuring contribution of Business analytics, Managing Changes. Descriptive Analytics, predictive analytics, predictive Modelling, Predictive analytics analysis, Data Mining, Data Mining Methodologies, Prescriptive analytics and its step in the business analytics Process, Prescriptive Modelling, nonlinear Optimization.

Faculty of Engineering, O.U. AICTE Model Curriculum with effect from Academic Year 2019-20

UNIT-IV

Forecasting Techniques: Qualitative and Judgmental Forecasting, Statistical Forecasting Models, Forecasting Models for Stationary Time Series, Forecasting Models for Time Series with a Linear Trend, Forecasting Time Series with Seasonality, Regression Forecasting with Casual Variables, Selecting Appropriate Forecasting Models.

Monte Carlo Simulation and Risk Analysis: Monte Carle Simulation Using Analytic Solver Platform, New-Product Development Model, Newsvendor Model, Overbooking Model, Cash Budget Model.

UNIT-V

Decision Analysis: Formulating Decision Problems, Decision Strategies with the without 8 Outcome Probabilities, Decision Trees, The Value of Information, Utility and Decision Making.

Recent Trends in Embedded and collaborative business intelligence, Visual data 4 recovery, Data Storytelling and Data journalism.

- 1. Business analytics Principles, Concepts, and Applications by Marc J. Schniederjans, Dara G. Schniederjans, Christopher M. Starkey, Pearson FT Press.
- 2. Business Analytics by James Evans, persons Education.

Course Code			Core/Elective				
OE 9103 EC		E	Open Elective				
Prerequisite	C	ontact Hou	ırs per We	ek	CIE	SEE	Credits
Trerequisite	L	Т	D	Credits			
-	3	-	-	-	70	3	

- > Detailed overview of important concepts of Embedded system
- > Analyse PIC microcontroller, its features and programming
- > Describe ARM Microcontroller architectural details and instruction set
- Understand ARM Memory management
- > Learn the techniques to develop an embedded system and case studies

Course Outcomes

After completing this course, the student will be able to:

- 1. Understand the fundamentals of the embedded system design
- 2. Enumerate the instruction set of ARM Processor by studying the architecture of ARM core
- 3. Acquire knowledge on the serial, parallel and network communication protocols.
- 4. Learn the embedded system design life cycle and co-design issues.
- 5. List the various embedded software development tools used in the design of embedded system for various applications.

UNIT I

Introduction to Embedded Systems: Overview of Embedded System Architecture, Challenges & Trends of Embedded Systems, Hardware Architecture, Software Architecture. Application areas of Embedded Systems and Categories of Embedded Systems. Embedded System Design and Co-Design issues and Design Cycle Process

UNIT II

PIC 18: Family Overview, Architecture, Instruction Set, Addressing modes. Timers, interrupts of PIC 18, Capture/Compare and PWM modules of PIC 18

UNIT III

ARM Architecture: ARM Design Philosophy, Registers, Program Status Register, Instruction Pipeline, Interrupts and Vector Table, Architecture Revision, ARM Processor Families. Instruction Set: Data Processing Instructions, Addressing Modes, Branch, Load, Store Instructions, PSR Instructions, Conditional Instructions.

UNIT IV

ARM Thumb Instruction Set: Register Usage, Other Branch Instructions, Data Processing Instruction Single-Register and Multi Register Load-Store Instructions, Stack, Software Interrupt Instructions. Exception and interrupt handling.

ARM Memory Management: Cache Architecture, Polices, Flushing and Caches, MMU, Page Tables, Translation Access Permissions, Context Switch.

UNIT V

Embedded Software Development Tools, Host and Target Machines, Linkers/Locators for Embedded Software, Getting Embedded Software into the Target System. Debugging Techniques.

Faculty of Engineering, O.U. AICTE Model Curriculum with effect from Academic Year 2019-20

Case Studies: Design of Embedded Systems using Microcontrollers – for applications in the area of communications and automotives. (GSM/GPRS, CAN, Zigbee)

- 1. Raj Kamal, Embedded Systems Architecture, Programming and Design, 2nd Edition, TMH, 2008.
- 2. Andrew N. Sloss, Dominic Symes, Chris Wright, ARM Systems Developer's Guides Designing & Optimizing System Software, Elsevier, 2008.
- 3. Mazidi, MCKinlay and Danny Causey, PIC Microcontrollers and Embedded Systems, Pearson Education, 2007
- 4. David.E. Simon, An Embedded Software Primer, 1st Edition, Pearson Education, 1999
- 5. Jonathan W. Valvano, Embedded Microcomputer Systems, Real Time Interfacing, Thomas Learning, 1999.

Course Code			Core/Elective							
OE 9104 EE			Open Elective							
Proroquisito	С	ontact Hou	ırs per We	ek	CIE	SEE	Credits			
Trerequisite	L T D P CIE SEE						Credits			
-	3	-	3							

> To enable students to aware about the generation of energy from the waste.

Course Outcomes

At the end of this course, students will be able to:

- 1. Students should able to learn the Classification of waste as a fuel.
- 2. Students should able to learn the Manufacture of charcoal.
- 3. Students should able to carry out the designing of gasifiers and biomass stoves.
- 4. Student should able to learn the Biogas plant technology.

UNIT-I

Introduction to Energy from Waste: Classification of waste as fuel – Agro based, Forest residue, Industrial waste - MSW – Conversion devices – Incinerators, gasifiers, digestors. Biomass Pyrolysis: Pyrolysis – Types, slow fast – Manufacture of charcoal – Methods - Yields and application – Manufacture of pyrolytic oils and gases, yields and applications.

UNIT-II

Biomass Gasification: Gasifiers – Fixed bed system – Downdraft and updraft gasifiers – Fluidized bed gasifiers – Design, construction and operation – Gasifier burner arrangement for thermal heating – Gasifier engine arrangement and electrical power – Equilibrium and kinetic consideration in gasifier operation.

UNIT-III

Biomass Combustion: Biomass stoves – Improved chullahs, types, some exotic designs, fixed bed combustors, Types, inclined grate combustors, Fluidized bed combustors, Design, construction and operation - Operation of all the above biomass combustors.

UNIT-IV

Biogas: Properties of biogas (Calorific value and composition) - Biogas plant technology and status - Bio energy system - Design and constructional features - Biomass resources and their classification - Biomass conversion processes - Thermo chemical conversion - Direct combustion - biomass gasification - pyrolysis and liquefaction

UNIT-V

Biochemical conversion: Anaerobic digestion - Types of biogas Plants – Applications - Alcohol production from biomass - Bio diesel production - Urban waste to energy conversion - Biomass energy programme in India.

- 1. Non-Conventional Energy, Desai, Ashok V., Wiley Eastern Ltd., 1990.
- 2. Biogas Technology A Practical Hand Book, Khandelwal, K. C. and Mahdi, S. S., Vol. I & II, Tata McGraw Hill Publishing Co. Ltd., 1983.
- 3. Food, Feed and Fuel from Biomass, Challal, D. S., IBH Publishing Co. Pvt. Ltd., 1991.
- 4. Biomass Conversion and Technology, C. Y. WereKo-Brobby and E. B. Hagan, John Wiley & Sons, 1996.

Course Code			Core/Elective				
OE 9105 ME			Open Elective				
Prerequisite	C	ontact Hou	ırs per We	ek	CIE	SEE	Credits
Trerequisite	L	Т	SEE	Credits			
-	3	-	3				

- > Causes for industrial accidents and preventive steps to be taken.
- > Fundamental concepts of Maintenance Engineering.
- > About wear and corrosion along with preventive steps to be taken
- > The basic concepts and importance of fault tracing.
- The steps involved in carrying out periodic and preventive maintenance of various equipments used in industry

Course Outcomes

After completing this course, the student will be equipped with:

- 1. concepts of engineering systems safety
- 2. Identify the causes for industrial accidents and suggest preventive measures.
- 3. Identify the basic tools and requirements of different maintenance procedures.
- 4. Apply different techniques to reduce and prevent Wear and corrosion in Industry.
- 5. Identify different types of faults present in various equipments like machine tools, IC Engines, boilers etc.
- 6. Apply periodic and preventive maintenance techniques as required for industrial equipments like motors, pumps and air compressors and machine tools etc.

UNIT-I

Industrial safety: Accident, causes, types, results and control, mechanical and electrical hazards, types, causes and preventive steps/procedure, describe salient points of factories act 1948 for health and safety, wash rooms, drinking water layouts, light, cleanliness, fire, guarding, pressure vessels, etc., Safety colour codes. Fire prevention and firefighting, equipment and methods.

UNIT-II

Fundamentals of maintenance engineering: Definition and aim of maintenance engineering, Primary and secondary functions and responsibility of maintenance department, Types of maintenance, Types and applications of tools used for maintenance, Maintenance cost & its relation with replacement economy, Service life of equipment.

UNIT-III

Wear and Corrosion and their prevention: Wear- types, causes, effects, wear reduction methods, lubricants-types and applications, Lubrication methods, general sketch, working and applications, i. Screw down grease cup, ii. Pressure grease gun, iii. Splash lubrication, iv. Gravity lubrication, v. Wick feed lubrication vi. Side feed lubrication, vii. Ring lubrication, Definition, principle and factors affecting the corrosion. Types of corrosion, corrosion prevention methods.

Faculty of Engineering, O.U.AICTE Model Curriculum with effect from Academic Year 2019-20UNIT-IV

Fault tracing: Fault tracing-concept and importance, decision tree concept, need and applications, sequence of fault finding activities, show as decision tree, draw decision tree for problems in machine tools, hydraulic, pneumatic, automotive, thermal and electrical equipment's like, I. Any one machine tool, ii. Pump iii. Aircompressor, iv. Internal combustion engine, v. Boiler, vi. Electrical motors, Types of faults in machine tools and their general causes.

UNIT-V

Periodic and preventive maintenance: Periodic inspection-concept and need, degreasing, cleaning and repairing schemes, overhauling of mechanical components, overhauling of electrical motor, common troubles and remedies of electric motor, repair complexities and its use, definition, need, steps and advantages of preventive maintenance. Steps/procedure for periodic and preventive maintenance of: I. Machine tools, ii. Pumps, iii. Air compressors, iv. Diesel generating (DG) sets, Program and schedule of preventive maintenance of mechanical and electrical equipment, advantages of preventive maintenance. Repair cycle concept and importance.

- 1. Maintenance Engineering Handbook, Higgins & Morrow, Da Information Services.
- 2. Maintenance Engineering, H. P. Garg, S. Chand and Company.
- 3. Pump-hydraulic Compressors, Audels, McGraw Hill Publication
- 4. Foundation Engineering Handbook, Winterkorn, Hans, Chapman & Hall London

Course Code			Core/Elective				
AD 9001 HS		Englis	Audit I				
D	C	ontact Hou	ırs per We	ek	CIE	SEE	Credits
rielequisite	L	Т	D	Р		SEE	Credits
-	2	-	-	-	30	70	-

- > Understand that how to improve your writing skills and level of readability
- > Understand the nuances of language and vocabulary in writing a Research Paper.
- > Develop the content, structure and format of writing a research paper.
- > Produce original research papers without plagiarism

Course Outcomes

After completing this course, the student will be able to:

- 1. Interpret the nuances of research paper writing.
- 2. Differentiate the research paper format and citation of sources.
- 3. To review the research papers and articles in a scientific manner.
- 4. Avoid plagiarism and be able to develop their writing skills in presenting theresearch work.
- 5. Create a research paper and acquire the knowledge of how and where to publish their original research papers.

UNIT - I

Academic Writing: Meaning & Definition of a research paper – Purpose of a research paper – Scope – Benefits, Limitations – outcomes.

UNIT - II

Research Paper Format: Title – Abstract – Introduction – Discussion – Findings, Conclusion – Style of Indentation – Font size/Font types – Indexing – Citation of sources.

UNIT - III

Research Methodology: Methods (Qualitative – Quantitative) Review of Literature. Criticizing, Paraphrasing & Plagiarism.

UNIT - IV

Process of Writing a research paper: Choosing a topic - Thesis Statement – Outline – Organizing notes - Language of Research – Word order, Paragraphs – Writing first draft –Revising/Editing - The final draft and proof reading.

UNIT - V

Research Paper Publication: Reputed Journals – National/International – ISSN No, No. of volumes, Scopus Index/UGC Journals – Free publications - Paid Journal publications – Advantages/Benefits **Presentation Skills:** Developing Persuasive Presentations, Structure of Presentation, Presentation Slides, Presentation Delivery, role of the audience, what to search and cite, how to establish credibility.

- 1. C. R Kothari, Gaurav, Garg, —Research Methodology Methods and Techniquesl, 4/e, New Age International Publishers.
- 2. Day R, -How to Write and Publish a Scientific Paper", Cambridge University Press, 2006
- 3. MLA Hand book for writers of Research Papers^{II}, 7/e, East West Press Pvt. Ltd, New Delhi
- 4. Lauri Rozakis, Schaum's, Quick Guide to Writing Great Research Papers^{II}, Tata McGraw Hills Pvt. Ltd, New Delhi.

Course Code			Core/Elective				
AD 9002 CE			Audit I				
Duran minita	C	ontact Hou	ırs per We	ek	CIE	SEE	Credits
Fletequisite	L	Т	D	Р	CIE	SEE	Credits
-	2	-	-	-	30	70	-

- To impart knowledge in students about the nature, causes, consequences and mitigation measures of the various natural disasters
- To enable the students to understand risks, vulnerabilities and human errors associated with human induced disasters
- To enable the students to understand and assimilate the impacts of any disaster on the affected area depending on its position/ location, environmental conditions, demographic, etc.

Course Outcomes

At the end of this course, students will be able to:

- 1. Learn to demonstrate a critical understanding of key concepts in disaster risk reduction
- 2. and humanitarian response.
- 3. Critically evaluate disaster risk reduction and humanitarian response policy and Practice from multiple perspectives.
- 4. Develop an understanding of standards of humanitarian response and practical relevance in specific types of disasters and conflict situations.
- **5.** Critically understand the strengths and weaknesses of disaster management approaches, planning and programming in different countries, particularly their home country or the countries they work in.

UNIT-I

Introduction: Disaster: Definition, Factors and Significance; Difference between Hazard and Disaster; Natural and Manmade Disasters: Difference, Nature, Types and Magnitude.

UNIT-II

Repercussions of Disasters and Hazards: Economic Damage, Loss of Human and Animal Life, Destruction of Ecosystem.

Natural Disasters: Earthquakes, Volcanisms, Cyclones, Tsunamis, Floods, Droughts and Famines, Landslides and Avalanches, Man-made disaster: Nuclear Reactor Meltdown, Industrial Accidents, Oil Slicks and Spills, Outbreaks of Disease and Epidemics, War and Conflicts.

UNIT-III

Disasters Prone Areas in India: Study of Seismic Zones; Areas Prone to Floods and Droughts, Landslides and Avalanches; Areas Prone to Cyclonic and Coastal Hazards with Special Reference to Tsunami; Post-Disaster Diseases and Epidemics

UNIT-IV

Disaster Preparedness: Monitoring of Phenomena Triggering a Disaster or Hazard; Evaluation of Risk: Application of Remote Sensing, Data from Meteorological and Other Agencies, Media Reports: Governmental and Community Preparedness.

UNIT-IV

Disaster Risk: Concept and Elements, Disaster Risk Reduction, Global and National Disaster Risk Situation. Techniques of Risk Assessment, Global Co-Operation in Risk Assessment and Warning, People's Participation in Risk Assessment. Strategies for Survival. Meaning, Concept and Strategies of Disaster Mitigation, Emerging Trends in Mitigation. Structural Mitigation and Non-Structural Mitigation, Programs of Disaster Mitigation in India.

- 1. R. Nishith, Singh AK, "Disaster Management in India: Perspectives, issues and strategies", New Royal Book Company.
- 2. Sahni, Pardeep (Eds.), "Disaster Mitigation Experiences and Reflections", PHI, New Delhi.
- 3. Goel S. L., "Disaster Administration and Management Text and Case Studies", Deep & Deep Publication Pvt. Ltd., New Delhi.

Course Code			Core/Elective				
AD 9003 HS		Sansl	Audit I				
Prerequisite	C	ontact Hou	ırs per We	æk	CIE	SEE	Credits
Trerequisite	L	Т	D	Р	CIL	SEE	Creatis
-	2	-	-	-	30	70	-

- > To get a working knowledge in illustrious Sanskrit, the scientific language in the world
- > To make the novice Learn the Sanskrit to develop the logic in mathematics, science & other subjects
- > To explore the huge knowledge from ancient Indian literature

Course Outcomes

At the end of this course, students will be able to:

- 1. Develop passion towards Sanskrit language
- 2. Decipher the latent engineering principles from Sanskrit literature
- 3. Correlates the technological concepts with the ancient Sanskrit history.
- 4. Develop knowledge for the technological progress
- 5. Explore the avenue for research in engineering with aid of Sanskrit

UNIT-I

Introduction to Sanskrit Language: Sanskrit Alphabets-vowels-consonants- significance of Amarakosaparts of Speech-Morphology-creation of new words-significance of synonyms-sandhi-samasa-sutras-active and passive Voice-Past/Present/Future Tense-Syntax-Simple Sentences (elementary treatment only)

UNIT-II

Role of Sanskrit in Basic Sciences: Brahmagupthas lemmas (second degree indeterminate equations), sum of squares of n-terms of AP- sulba, sutram or baudhayana theorem (origination of Pythagoras theorem)-value of pie-Madhava's sine and cosine theory (origination of Taylor's series).

The measurement system-time-mass-length-temp, Matter elasticity-optics-speed of light (origination of Michaelson and Morley theory).

UNIT-III

Role of Sanskrit in Engineering-I (Civil, Mechanical, Electrical and Electronics Engineering):

Building construction-soil testing-mortar-town planning-Machine definition-crucible-furnace-air blower-Generation of electricity in a cell-magnetism-Solar system-Sun: The source of energy, the earth-Pingala chandasutram (origination of digital logic system)

UNIT-IV

Role of Sanskrit in Engineering-II (Computer Science Engineering & Information Technology): Computer languages and the Sanskrit languages-computer command words and the vedic command wordsanalogy of pramana in memamsa with operators in computer language-sanskrit analogy of physical sequence and logical sequence, programming.

UNIT-V

Role of Sanskrit in Engineering-III (Bio-technology and Chemical Engineering): Classification of plants- plants, the living-plants have senses-classification of living creatures, Chemical laboratory location and layout- equipment-distillation vessel-kosthi yanthram

- 1. M Krishnamachariar, History of Classical Sanskrit Literature, TTD Press, 1937.
- 2. M.R. Kale, A Higher Sanskrit Grammar: For the Use of School and College Students, Motilal Banarsidass Publishers, 2015.
- 3. Kapail Kapoor, Language, Linguistics and Literature: The Indian Perspective, ISBN- 10: 8171880649, 1994.
- 4. Pride of India, Samskrita Bharati Publisher, ISBN: 81-87276 27-4, 2007.
- 5. Shri Rama Verma, Vedas the source of ultimate science, Nag publishers, 2005.

Course Code			Core/Elective					
AD 9004 HS			Audit I					
Prerequisite	C	ontact Hou	ırs per We	ek	CIE	SEE	Credits	
Trerequisite	L	Т	Credits					
-	2	2 30 70						

- > Understand the need and importance of Values for self-development and for National development.
- ➢ Imbibe good human values and Morals
- > Cultivate individual and National character.

Course Outcomes

After completion of the course, students will be able to:

- 1. Gain necessary Knowledge for self-development
- 2. Learn the importance of Human values and their application in day to day professional life.
- 3. Appreciate the need and importance of interpersonal skills for successful career and social life
- 4. Emphasize the role of personal and social responsibility of an individual for all-round growth.
- 5. Develop a perspective based on spiritual outlook and respect women, other religious practices, equality, non-violence and universal brotherhood.

UNIT-I

Human Values, Ethics and Morals: Concept of Values, Indian concept of humanism, human values; Values for self-development, Social values, individual attitudes; Work ethics, moral and non- moral behaviour, standards and principles based on religion, culture and tradition.

UNIT-II

Value Cultivation, and Self-management: Need and Importance of cultivation of values such as Sense-of Duty, Devotion to work, Self-reliance, Confidence, Concentration, Integrity & discipline, and Truthfulness.

UNIT-III

Spiritual outlook and social values: Personality and Behavior, Scientific attitude and Spiritual (soul) outlook; Cultivation of Social Values Such as Positive Thinking, Punctuality, Love & Kindness, avoiding fault finding in others, Reduction of anger, forgiveness, Dignity of labour, True friendship, Universal brotherhood and religious tolerance.

UNIT-IV

Values in Holy Books: Self-management and Good health; internal & external cleanliness, Holy books versus Blind faith, Character and Competence, Equality, Nonviolence, Humility, Role of Women.

UNIT-V

Dharma, Karma and Guna: Concept of soul; Science of Reincarnation, Character and Conduct, Concept of Dharma; Cause and Effect based Karma Theory; The qualities of Devine and Devilish; Satwic, Rajasic and Tamasic gunas.

- 1. Chakroborty, S.K., Values & Ethics for organizations Theory and practicel, Oxford University Press, New Delhi, 1998.
- 2. Jaya Dayal Goyandaka, Srimad Bhagavad Gita with Sanskrit Text, Word Meaning and Prose Meaning, Gita Press, Gorakhpur, 2017.

Course Code			Core/Elective				
AD 9011 HS	Co	onstitutio	Audit II				
Durana arriaita	C	ontact Hou	ırs per We	ek	CIE	SEE	Credits
Trerequisite	L	Т	D	Р	CIL	SEE	Credits
-	2	-	-	-	30	70	-

Understand the premises informing the twin themes of liberty and freedom from a civil rights perspective and to address the growth of Indian opinion regarding modern Indian intellectuals' constitutional role and entitlement to civil and economic rights as well as the emergence of nationhood in the early years of Indian nationalism.

Course Outcomes

At the end of this course, students will be able to:

- 1. Discuss the growth of the demand for civil rights in India for the bulk of Indians before the arrival of Gandhi in Indian politics.
- 2. Discuss the intellectual origins of the framework of argument that informed the conceptualization of social reforms leading to revolution in India.
- **3.** Discuss the circumstances surrounding the foundation of the Congress Socialist Party [CSP] under the leadership of Jawaharlal Nehru and the eventual failure of the proposal of direct elections through adult suffrage in the Indian Constitution.
- 4. Discuss the passage of the Hindu Code Bill of 1956.

UNIT-I

History of Making of the Indian Constitution: History, Drafting Committee, (Composition & Working) Philosophy of the Indian Constitution: Preamble, Salient Features.

UNIT-II

Contours of Constitutional Rights & Duties: Fundamental Rights, Right to Equality, Right to Freedom, Right against Exploitation, Right to Freedom of Religion, Cultural and Educational Rights, Right to Constitutional Remedies, Directive Principles of State Policy, Fundamental Duties.

UNIT-III

Organs of Governance: Parliament, Composition, Qualifications and Disqualifications, Powers and Functions, Executive, President, Governor, Council of Ministers, Judiciary, Appointment and Transfer of Judges, Qualifications. Powers and Functions.

UNIT-IV

Local Administration: District's Administration head: Role and Importance, Municipalities: Introduction, Mayor and role of Elected Representative CEO of Municipal Corporation, Panchayat raj: Introduction, PRI: Zilla Panchayat, Elected officials and their roles, CEO Zilla Panchayat: Position and role. Block level: Organizational Hierarchy (Different departments), Village level: Role of Elected and Appointed officials, Importance of grass root democracy.

UNIT-V

Election Commission: Election Commission: Role and Functioning. Chief Election Commissioner and Election Commissioners. State Election Commission: Role and Functioning. Institute and Bodies for the welfare of SC/ST/OBC and women.

- 1. The Constitution of India, 1950 (Bare Act), Government Publication.
- 2. Dr. S. N. Busi, Dr. B. R. Ambedkar framing of Indian Constitution, 1st Edition, 2015.
- 3. M. P. Jain, Indian Constitution Law, 7th Edn., Lexis Nexis, 2014.
- 4. D.D. Basu, Introduction to the Constitution of India, Lexis Nexis, 2015.

Course Code			Core/Elective				
AD 9012 HS			Audit II				
Prerequisite	C	ontact Hou	ırs per We	ek	CIE	SEE	Credits
Trerequisite	L	Т	D	Р	CIL	SEE	Credits
-	2	-	-	-	30	70	-

- > To present the basic concepts of design and policies of pedagogy studies.
- To provide understanding of the abilities and dispositions with regard to teaching techniques, curriculum design and assessment practices.
- > To familiarize various theories of learning and their connection to teaching practice.
- > To create awareness about the practices followed by DFID, other agencies and other researchers.
- > To provide understanding of critical evidence gaps that guides the professional development

Course Outcomes

At the end of this course, students will be able to:

- 1. Illustrate the pedagogical practices followed by teachers in developing countries both in formal and informal classrooms.
- 2. Examine the effectiveness of pedagogical practices.
- 3. Understand the concept, characteristics and types of educational research and perspectives of research.
- 4. Describe the role of classroom practices, curriculum and barriers to learning.
- 5. Understand Research gaps and learn the future directions.

UNIT-I

Introduction and Methodology: Aims and rationale, Policy background, Conceptual framework and terminology - Theories of learning, Curriculum, Teacher education - Conceptual framework, Research questions, Overview of methodology and Searching.

UNIT-II

Thematic Overview: Pedagogical practices followed by teachers in formal and informal classrooms in developing countries - Curriculum, Teacher education

UNIT-III

Evidence on the Effectiveness of Pedagogical Practices: Methodology for the in depth stage: quality assessment of included studies - How can teacher education (curriculum and Practicum) and the school curriculum and guidance material best support effective pedagogy? - Theory of change - Strength and nature of the body of evidence for effective pedagogical practices - Pedagogic theory and pedagogical approaches – Teachers attitudes and beliefs and pedagogic strategies.

UNIT-IV

Professional Development: alignment with classroom practices and follow up support - Support from the head teacher and the community – Curriculum and assessment - Barriers to learning: Limited resources and large class sizes.

UNIT-V

Research Gaps and Future Directions: Research design – Contexts – Pedagogy - Teacher education - Curriculum and assessment – Dissemination and research impact.

- 1. Ackers J, Hardman F, Classroom Interaction in Kenyan Primary Schools, Comparell, 31 (2): 245 261, 2001.
- 2. Agarwal M, Curricular Reform in Schools: The importance of evaluation, Journal of Curriculum Studies, 36 (3): 361 379, 2004.
- 3. Akyeampong K, Teacher Training in Ghana does it count? Multisite teacher education research project (MUSTER), Country Report 1. London: DFID, 2003.
- 4. Akyeampong K, Lussier K, Pryor J, Westbrook J, Improving teaching and learning of Basic Maths and Reading in Africa: Does teacher Preparation count? International Journal Educational Development, 33 (3): 272- 282, 2013.
- 5. Alexander R J, Culture and Pedagogy: International Comparisons in Primary Education, Oxford and Boston: Blackwell, 2001.
- 6. Chavan M, Read India: A mass scale, rapid, learning to read campaign, 2003.

Course Code			Core/Elective				
AD 9013 HS		St	Audit II				
D	C	ontact Hou	ırs per We	æk	CIE	SEE	Credits
rierequisite	L	Т	SEE	Credits			
-	2	-	-	-	30	70	-

The Course will introduce the students to

- > Creating awareness about different types of stress and the role of yoga in the management of stress.
- Promotion of positive health and overall wellbeing (Physical, mental, emotional, social and spiritual).
- > Prevention of stress related health problems by yoga practice.

Course Outcomes

After successful completion of the course, the students will be able to:

- 1. Understand yoga and its benefits.
- 2. Enhance Physical strength and flexibility.
- 3. Learn to relax and focus.
- 4. Relieve physical and mental tension through asanas.
- 5. Improve work performance and efficiency.

UNIT - I

Meaning and Definition of Yoga - Historical perspective of Yoga - Principles of Astanga Yoga by Patanjali.

UNIT - II

Meaning and Definition of Stress - Types of stress - Eustress and Distress. Anticipatory Anxiety and Intense Anxiety and depression. Meaning of Management- Stress Management.

UNIT - III

Concept of Stress According to Yoga - Stress assessment methods - Role of Asana, Pranayama and Meditation in the management of stress

UNIT - IV

Asanas - (5 Asanas in each posture) - Warm up - Standing Asanas - Sitting Asanas - Prone Asanas - Supine asanas - Surya Namaskar.

UNIT - V

Pranayama - Anulom and Vilom Pranayama - Nadishudhi Pranayama - Kapalabhati Pranayama - Bhramari Pranayama - Nadanusandhana Pranayama.

Meditation Techniques: Om Meditation - Cyclic meditation: Instant Relaxation technique (QRT), Quick Relaxation Technique (QRT), Deep Relaxation Technique (DRT)

- 1. "Yogic Asanas for Group Training Part-I", Janardhan Swami Yogabhyasi Mandal, Nagpur.
- 2. Swami Vivekananda, "Rajayoga or Conquering the Internal Nature", Advaita Ashrama (Publication Department), Kolkata.
- 3. Nagendra H.R and Nagaratna R, "Yoga Perspective in Stress Management", Swami Vivekananda Yoga Prakashan, Bangalore.

Course Code		Course Title					
AD 9014 HS	Person	ality Dev	Audit II				
Proroquisito	C	Contact Hours per Week					
rierequisite	L	Т	D	Р		SEE	Credits
-	2	-	-	-	30	70	-
 Course Objectives ➢ To learn to achieve the highest goal happily ➢ To become a person with stable mind, pleasing personality and determination 							

> To awaken wisdom in students

Course Outcomes

At the end of this course, students will be able to:

- 1. Develop their personality and achieve their highest goal of life.
- 2. Lead the nation and mankind to peace and prosperity.
- 3. Practice emotional self-regulation.
- 4. Develop a positive approach to work and duties.
- 5. Develop a versatile personality.

UNIT - I

Neetisatakam – Holistic Development of Personality - Verses 19, 20, 21, 22 (Wisdom) - Verses 29, 31, 32 (Pride and Heroism) - Verses 26,28,63,65 (Virtue)

UNIT - II

Neetisatakam – Holistic Development of Personality (cont'd) - Verses 52, 53, 59 (don'ts) - Verses 71,73,75 & 78 (do's) - Approach to day to day works and duties.

UNIT - III

Introduction to Bhagavadgeetha for Personality Development - Shrimad Bhagavadgeetha: Chapter 2 – Verses 41, 47, 48 - Chapter 3 – Verses 13,21,27,35 - Chapter 6 – Verses 5,13,17,23,35 - Chapter 18 – Verses 45, 46, 48 Chapter – 6: Verses 5, 13, 17, 23, 35; Chapter – 18: Verses 45, 46, 48

UNIT - IV

Statements of Basic Knowledge - Shrimad Bhagavadgeetha: Chapter 2- Verses 56, 62,68 - Chapter 12 – Verses 13, 14, 15, 16, 17, 18 - Personality of Role model from Shrimad Bhagawat Geeta.

UNIT - V

Role of Bhagavadgeetha in the Present Scenario - Chapter 2 – Verses 17 - Chapter 3 – Verses 36, 37, 42 - Chapter 4 – Verses 18, 38, 39 - Chapter 18 – Verses 37, 38, 63.

Suggested Readings:

- 1. Srimad Bhagavad Gita, Swami Swarupananda Advaita Ashram (Publication Department), Kolkata
- 2. Bhartrihari's Three Satakam (Niti-sringar-vairagya), P.Gopinath, Rashtriya Sanskrit Sansthanam, New Delhi

Online Resources: NTPEL: http://nptel.ac.in/downloads/109104115/

Course Code			Core/Elective				
PC 3251 ES		E	Core				
Prerequisite	C	Contact Hours per Week				SEE	Credits
rierequisite	L	T D P			CIE	SEE	Credits
-	-	-	-	2	50	-	1

Course Outcomes

After completing this course, the student will be able to:

- 1. Write programs for interfacing applications
- 2. Experience with a set of tools for embedded systems programming and debugging
- 3. Experience with implementing several embedded systems with particular focus on the interaction between multiple devices.
- 4. Write programs of serial communication
- 5. Design prototypes using microcontrollers and various analog and digital ICs

Cycle 1: Programming in 8051

- 1. Study of 8051 Evaluation Board Trainer kit and Keil IDE Software Tool.
- 2. Serial Data Transmission
- 3. Interface switches and LEDs
- 4. Interface LCD
- 5. Interface 4*4 matrix keyboard
- 6. Interface stepper motor
- 7. Interface 7 Segment Display using I2C
- 8. ADC, DAC Interface

Cycle 2: Programming in LPC2148 ARM Processor

- 1. Configure and Control General Purpose I/O Pins
- 2. Interfacing LED & Switch Interface
- 3. 2*16 LCD Display
- 4. Serial Communication
- 5. I2C Interface & EEPROM Interface
- 6. Buzzer Interface
- 7. SD-MMC Card Interface

Note: all the experiments are to be carried out independently by each student with different specifications. At least 12 experiments are to be carried out.

Course Code		Course Title					
PC 3252 ES			Ι	oT Lab			Core
Proroquisito	Co	ontact Hou	ırs per We	ek	CIE	SEE	Cradita
rierequisite	L	Т	D	Р	CIL	SEE	Credits
-	-	2 50 -					1
Course Outcomes							
After completing thi	is course, t	he student	will be ab	ole to:			
1. Experience	ESP32/ES	ESP32/ESP8266					
2. Experience	vith implementing several IoT applications						
3. Work with o	different co	ommunicat	tion techni	iques			

- 4. Send data to mobile application
- 5. Send data to webpage

Note: Following are some of the programs that a student should be able to write and test on an ESP32/ESP3266, but not limited to this only.

Cycle 1:

- 1. Use of ESP32/ESP8266 and Arduino IDE. Interface ESP32/ESP8266 with Arduino IDE.
- 2. Interface LED and Switch to ESP32/ESP8266.
- 3. Flash an LED at given on time and off time cycle.
- 4. Interface LCD and GLCD with ESP32/ESP8266 to display text data and graphical data.
- 5. Interface Temperature and humidity sensors to ESP32/ESP8266 using ADC.
- 6. Interface GSM module to ESP32/ESP8266 using UART protocol.

Cycle 2:

- 7. RTC interface with ESP32/ESP8266 to display time and date on LCD using I2C protocol.
- 8. External memory interfacing to ESP32/ESP8266 for storing data using SPI protocol.
- 9. Interface Bluetooth to send data from ESP32/ESP8266 to mobile application.
- 10. LoRa (SX1276/78) interface with ESP32/ESP8266 to send data from transmission node to receiver node.
- 11. Interface any sensor to ESP32/ESP8266 and send the sensor data to webpage using TCP/IP or HTTP protocol.
- 12. Control a light source using web page.
- 13. Case study-1
- 14. Case study-2.

Additional programs:

- 1. GPRS interfacing with ESP32/ESP8266.
- 2. Control relay using ESP32/ESP8266 and webpage/mobile app.

Note: all the experiments are to be carried out independently by each student with different specifications. At least 12 experiments are to be carried out.

Course Code			Core/Elective				
PC 3253 ES		E	Core				
Prerequisite	C	Contact Hours per Week				SEE	Credits
Trerequisite	L	Т	D	Р		SEE	Credits
-	-	2 50 -					1
Course Outcomes							
After completing this course, the student will be able to:							

- 1. Demonstrate Task Management and Inter-Task Communication
- 2. Demonstrate Message queues and priority inversion
- 3. Experience the PSOC
- 4. Design dynamic architectures using FPGA
- 5. Design arithmetic unit

Cycle 1: Real Time Operating System (RTOS):

- 1. Multitasking
- 2. Task and Button-Parallel Execution
- 3. Task and Multiple Interrupts Parallel Execution
- 4. Message Queues
- 5. Priority Inversion

Cycle 2:

Part-1: Understanding PSOC

- 1. Study and Characterization of programmable gain amplifier (PGA)
- 2. On chip ADC and DAC Realization
- 3. LED Control and Pattern Generation

Part-2 FPGA Programming:

- 1. Design Simulation & synthesis of combinational circuits 5. Design Simulation & synthesis of sequential circuits
- 2. Design VGA controller.
- 3. Designing UART interface on SOC platform.
- 4. Designing basic DSP designs like adder, multiplier, etc.;
- 5. Design and development of FIR filter.

Note: all the experiments are to be carried out independently by each student with different specifications. At least 12 experiments are to be carried out.

Course Code			Core/Elective					
PC 3254 ES			Core					
Proroquisito	Co	ontact Hou	ırs per We	æk	CIE	SEE	Cradita	
Prerequisite	L	Т	D	Р	CIE	SEE	Creans	
-	-	-	-	2	50	-	1	
Course Outcomes								
At the end of this course, students will be able to:								
1. Develop the habit of referring the journals for literature review.								

- 2. Understand the gist of the research paper.
- 3. Identify the potential for further scope.
- 4. Present the work in an efficient manner.
- 5. Write the documentation in standard format.

Seminar topics may be chosen by the students with advice from the faculty members and the student shall read further relevant articles in the domain.

The seminar must be clearly structured and the power point presentation shall include following aspects:

- 1. Introduction to the field
- 2. Literature survey
- 3. Consolidation of available information
- 4. Summary and Conclusions
- 5. References

Each student is required to:

- 1. Deliver the seminar for a maximum duration of 30 minutes, where the presentation should be for 20 minutes in PowerPoint, followed by Question and Answers session for 10 minutes.
- 2. Submit the detailed report of the seminar in spiral bound in a précised format as suggested by the Department.

	Guidelines for awarding marks							
S. No.	Description	Max. Marks						
1	Contents and relevance	10						
2	Presentation skills	10						
3	Preparation of PPT slides	05						
4	Questions and answers	05						
5	Report in a prescribed format	20						

Note:

- 1. The seminar presentation should be a gist of at least five research papers from **Peer-reviewed** or **UGC recognised** journals.
- 2. The seminar report should be in the following order: Background of work, literature review, techniques used, prospective deliverables, discussion on results, conclusions, critical appraisal and reference.
- 3. At least two faculty members will be associated with the seminar presentation to evaluate and award marks.
- 4. Attendance of all the students for weekly seminar presentations is compulsory. If the student fails to secure minimum attendance as per O.U. rules, the marks awarded in the seminar presentation shall remain void.

Course Code		Course Title						
PC 3255 ES		Ν	Core					
Prerequisite	Co	ontact Ho	urs per We	ek	CIE	SEE	Credits	
Terequisite	L	Т	D	Р			Crouits	
-	-	-	-	4	50	-	2	
Course Outcomes								
At the end of this course, students will be able to:								
1. Formulate a specific problem and give solution								

- 2. Develop model/models either theoretical/practical/numerical form
- 3. Solve, interpret/correlate the results and discussions
- 4. Conclude the results obtained
- 5. Write the documentation in standard format

Guidelines:

- As part of the curriculum in the II- semester of the programme each student shall do a mini project, generally comprising about three to four weeks of prior reading, twelve weeks of active research, and finally a presentation of their work for assessment.
- Each student will be allotted to a faculty supervisor for mentoring.
- Mini projects should present students with an accessible challenge on which to demonstrate competence in research techniques, plus the opportunity to contribute something moreoriginal.
- Mini projects shall have inter-disciplinary/ industry relevance.
- The students can select a mathematical modeling based/Experimental investigations or Numerical modeling
- All the investigations should be clearly stated and documented with the reasons/explanations.
- The mini-project shall contain a clear statement of the research objectives, background of work, literature review, techniques used, prospective deliverables, and detailed discussion on results, conclusions and reference

Guidelines for awarding marks in CIE (Continuous Internal Evaluation): Max. Marks: 50						
Evaluation by	Max. Marks	Evaluation Criteria / Parameter				
Supervisor	20	Progress and Review				
Supervisor	05	Report				
	05	Relevance of the Topic				
	05	PPT Preparation				
Departmental Committee	05	Presentation				
	05	Question and Answers				
	05	Report Preparation				

Departmental committee: Supervisor and a minimum of two faculty members

Course Code			Core/Elective				
PC 3256 ES			Core				
Proroquisito	C	Contact Hours per Week				SEE	Cradita
rierequisite	L	Т	D P CIE SEE				Credits
-	-	-	-	20	100	-	10

Course Outcomes

At the end of this course, students will be able to:

- 1. Exposed to self-learning various topics.
- 2. Learn to survey the literature such asbooks, journals and contact resource persons for the selected topic of research.
- 3. Learn to write technical reports.
- 4. Develop oral and written communication skills to present.
- 5. Defend their work in front of technically qualified audience

Guidelines:

- The Project work will preferably be a problem with research potential and should involve scientific research, design, generation/collection and analysis of data, determining solution and must preferably bring out the individual contribution.
- Seminar should be based on the area in which the candidate has undertaken the dissertation work.
- The CIE shall include reviews and the preparation of report consisting of a detailed problem statement and a literature review.
- The preliminary results (if available) of the problem may also be discussed in the report.
- The work has to be presented in front of the committee consists of Chairperson-BoS, Osmania University and Head, Supervisor & Project coordinator from the respective Department of the Institute.
- The candidate has to be in regular contact with his supervisor and the topic of dissertation must be mutually decided by the guide and student.

Guidelines for awarding marks in CIE (Continuous Internal Evaluation): Max. Marks: 10							
Evaluation by	Max. Marks	Evaluation Criteria / Parameter					
Supervisor	30	Project Status / Review(s)					
Supervisor	20	Report					
Departmental Committee	10	Relevance of the Topic					
(Chairperson BoS, Osmania	10	PPT Preparation					
University and Head,	10	Presentation					
Supervisor & Project	10	Question and Answers					
coordinator from the respective	10	Report Preparation					
department of the institution)	10	Report Freparation					

Note: The Supervisor has to assess the progress of the student regularly.

Course Code			Core/Elective				
PC 3257 ES		1	Core				
Prerequisite	C	Contact Hours per Week					Credits
Trerequisite	L	Т	D	Р	CIL	SEE	Credits
-	-	-	-	32	-	200	16

Course Outcomes

At the end of this course, students will be able to:

- 1. Use different experimental techniques and will be able to use different software/ computational /analytical tools.
- 2. Design and develop an experimental set up/ equipment/test rig.
- 3. Conduct tests on existing set ups/equipments and draw logical conclusions from the results after analysing them.
- 4. Either work in a research environment or in an industrial environment.
- 5. Conversant with technical report writing and will be able to present and convince their topic of study to the engineering community.

Guidelines:

- It is a continuation of Major Project Phase I started in semester III.
- The student has to submit the report in prescribed format and also present a seminar.
- The dissertation should be presented in standard format as provided by the department.
- The candidate has to prepare a detailed project report consisting of introduction of the problem, problem statement, literature review, objectives of the work, methodology (experimental set up or numerical details as the case may be) of solution and results and discussion.
- The report must bring out the conclusions of the work and future scope for the study. The work has to be presented in front of the examiners panel consisting of an approved external examiner and Chairperson BoS, & Head, Osmania University and Supervisor from the Institute.
- The candidate has to be in regular contact with his/her Supervisor / Co- Supervisor

Guidelines for awarding marks in SEE (Semester End Examination): Max. Marks: 20						
Evaluation by	Max. Marks	Evaluation Criteria / Parameter				
	10	Regularity and Punctuality				
	10	Work Progress				
Supervisor	30	Quality of the work which may lead to publications				
Supervisor	10	Analytical / Programming / Experimental Skills				
	10	Preparation				
	10	Report preparation in a standard format				
External Examiner	20	Power Point Presentation				
and Chairperson,	60	Quality of thesis and evaluation				
BoS & Head,	30	Innovations, application to society and Scope for				
Osmania University	30	future study				
(All together)	20	Viva-Voce				